JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES
(An Autonomous Institution)

The following list of Value Added courses organized by the Institution with Brochure,contents and outcomes for the academic year 2017-18
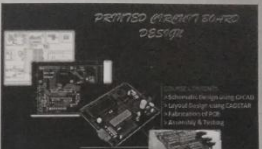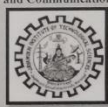
JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES

UGC - AUTONOMOUS

Permanently Affiliated to JNTUH, Accredited by NBA, Recognised Under UGC 2 (f) & 12 (b)

NARSAMPET, WARANGAL - 506332

Estd:2001

CIRCUIT SIMULATION & LAYOUT DESIGN

Eligibility:

The program is open to the all students of Electronics & Communication Engineering courses at undergraduate and post graduate level re welcome.

Registration Details:

- Students   100/-

How to Register:

The participants may log on to the website: www.jits.in and fill up the application form selecting the name of the course you may also send scanned copy of your filled application form Application format may be downloaded from the website(also available in this Boucher) . you can pay the fee through google/ phone pay: 9618336977

Address for Correspondence:
The Convener,
Head of the Department
Dept. of ECE
Jayamukhi Institute of Technological Sciences, Narsampet, (Dist): Warangal-506332. (T.S)
E-mail: hameedjits@gmail.com
Mobile: 9490377766.
Mobile: 7569839692.

Chief Patron:

Sri Ch.Narsimha Reddy - President

Patron:

Sri Ch.Devender Reddy - Secretary
Sri T.V.R.N.Reddy - Joint-Secretary

Principal

Dr. M.Lokanadha Rao

Director

Prof.P.Rangaiah,

Convener:
Mr.Md.Hameed Pasha
Associate Professor &Head, ECE Department
Coordinators:
Mrs.S.Sandhya Rani
Assoc.Prof,ECE Department
Cell: 9030073969, 9849829390.
Mr.M.Sridhar
Asst.Prof,ECE Department
Cell: 9491617090
Schedule:

1. Last date of receipt of applications:
   10th March 2018

2. Spot registrations allowed

The Applications in the prescribed form has to be reach coordinator either Email and/ or by hard copy.

One week WORKSHOP ON
"Circuit Simulation & Layout Design"
10th March -15th March,2018

Organized by:

DEPARTMENT OF
Electronics and Communication Engineering

JAYAMUKHI

INSTITUTE OF TECHNOLOGICAL SCIENCES

(U.G.C AUTONOUMOUS)

Permanently Affiliated to JNTUH, Accredited by NBA

Narsampet, Warangal – 506332.
Tel (08718) 208586, Fax: (08718) 222569
Website: www.jits.in

About JITS Narsampet

Jayamukhi Institute of Technological Sciences, Narsampet Sponsored by the Jayamukhi Educational Society, the Jayamukhi Institute of Technological Sciences came into being in 2001 to provide quality and contemporary education with social relevance in the engineering faculty with an ultimate vision to maintain global standards in higher learning and research. The Institute has the approval of AICTE and recognized by the Government of Telangana. It is permanently affiliated to Jawaharlal Nehru Technological University (JNTU), Hyderabad, UGC Autonomous & accredited by NBA.

The Institute has come upon 40 acres of green pastures in Narsampet, about 30km away from the historic city of Warangal and presents a picturesque and panoramic view. JITS offers a four-year B.Tech. programme in the disciplines of CSE, ECE, EEE,CIVIL and ME

Department of ECE

Department of Electronics and Communication Engineering has set vision "To be a premier department for education in Electronics and Communication Engineering in state of Telangana, moulding students into professional engineers". The department imparts state of the art education and encourages cutting edge research. It offers UG program, PG programs in VLSI and ES. Well equipped Laboratories have been facilitated for the Students, research Scholars and the 50Lakhs of funds generated from AICTE under various schemes like MODROBS, FDPs, STTPs. Received TEQIP-III fund from JNTU Hyderabad,

Established two laboratories namely Internet of Things(IoT) and Robotics sponsored by Texas Instruments USA.

About the Workshop:

Over the years circuit board manufacturing has continued to grow in order to keep up with the increasing demands of newer, faster, and more complex electronic circuitry. How a PCB is created for it to do what is required is a subject that could fill a library, but we will give you the basics of what it takes to design a printed circuit board here.

PCB Design has been working to transform Engineering education in India from theoretical and practical teaching to the collaboration gives opportunities to develop the students, Teachers and industrialists. The main objective of learning the development process is to launch onto the production line and in the market.

List The following list of resource Person:

1. Prof.K.Kishan Rao, Professor, SNIST, Hyderabad
2. Dr.P.Krishna, Professor, of ECE, JITS
3. Dr.M. Krishna Samy, Professor of ECE,JITS
4. Prof.M.Sushanth Babu, Professor of EC E, JITS
5. Prof.A.Janardhan, Professor of ECE, JITS

One week WORKSHOP ON
"Circuit Simulation & Layout Design"
10th March -15th March,2018

REGISTRATION FORM

1. Name of Participant: ....
2. Roll No: ....
3. Institution: ....
4. Address for Correspondence: ....
   ....
   ....
5. Mobile No: ....
6. Email: ....
7. Category : ....

DECLARATION

The information provided herewith is true to the best of my knowledge. I agree to abide by the rules and regulations governing the workshop programme.

Date:

Place: **Signature of the applicant**

Signature of the Sponsoring Authorities

JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES

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NARSAMPET, WARANGAL - 506332

Estd:2001

CIRCUIT SIMULATION & LAYOUT DESIGN

CONTENTS:

1.Introduction to Computer Aided Design of electronics

2.Fundamental of basic electronics

- Component identification

- Component symbols & their footprints

3.Schematic Design using OrCAD

4.Layout Design using CADSTAR

5.Design rule checking

6.Manufacturing exports

7.Fabrication of PCB

8.Assembling & Testing

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NARSAMPET, WARANGAL - 506332

Estd:2001

CIRCUIT SIMULATION & LAYOUT DESIGN

COURSE OUTCOMES

Upon the completion of this course, students will able to:

CO 1 : Understand the various symbols, foot prints/packaging of components

CO 2: Design schematic and layout of electronic circuits

CO 3: Fabricate Printed Circuit Board

CO 4: Evaluate and test the completed PCB

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NARSAMPET, WARANGAL - 506332

Estd:2001

Brain wave Robotics

Eligibility:
The program is open to the all students members of Electronics and Communication Engineering courses at undergraduate and post graduate level are welcome.

Registration Details:
IEEE /ISTE Students Free   Non IEEE /ISTE Students   Free

How to Register:
The participants may log on to the website: www.jits.in and fill up the application form selecting the name of the course you may also send scanned copy of your filled application form to tabbusmile786@gmail.com or ecehod@jits.in. Application format may be downloaded from the website (also available in this brochure).

Address for Correspondence:
Head of the Department
Md. Hameed Pasha
Dept. of Electronics and Communication Engineering
Jayamukhi Institute of Technological Sciences, Narsampet, (Dist): Warangal-506332. (T.S)
Email: hodecejits@gmail

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Chief Patron:
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Principal
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Director
Prof. P. Rangaiah,

Convener:
Mr. Md. Hameed Pasha
Associate Professor & Head, Dept. of ECE
Coordinators :
Ms. Zulekha Tabassum, Assoc.Prof
Dept. of ECE
Mobile: +91-9949676900
Ms. K.Sujatha, Asst.Prof
Dept. of ECE
Mobile: +91-9440715487
E-Mail: ecehod@jits.in

Schedule:
Last date of receipt of applications:
17th September, 2017
Spot registrations allowed
The Applications in the prescribed form has to be reach coordinator either Email and/ or by hard copy.
Programme sponsored by AICTE and many workshops, seminars and guest lectures for the benefits of Students.

About the Workshop:
The mechanism is initiated to implant since 30 years and with growing conceptualization in it, various fundamentals are coming up. Brain consists of various parts and the three main segments are cerebrum, cerebellum and brain stem. Each thought generated in the brain has some energy associated with it. This energy is in the form of an electrical signal which can be detected. This signal generated is defined as electroencephalogram signal in terms of neurology. This electrical signal has some voltage associated with it and it can be detected using electrodes placed across the brain.

HANDS-ON WORKSHOP ON "BRAINWAVE ROBOTICS"
18th -22nd September, 2017

Organized by:

DEPARTMENT OF
Electronics and Communication Engineering
&
IETE STUDENT CHAPTER

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NARSAMPET, WARANGAL - 506332

Estd:2001

Brain wave Robotics

CONTENTS:

1. Introduction to Embedded Systems
2. Application of Embedded Systems in Robotics
3. Introduction to Robotics,,
4. Operator Interface,
5. Introduction to Arduino,
6. Artificial Intelligence
7. Introdcution to IoT,
8. **Robotics Applications**
9. Machine Learning in Robotics,
10. Programming

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NARSAMPET, WARANGAL - 506332

Estd:2001

Brain wave Robotics

COUSRE OUTCOMES

1. Able to apply robot fundamentals in designing various types of end effecters
2. Able to design end effects required for Artificial Intelligence applications
3. Able to determine the robot trajectory to robotic motion by applying Robotic basics
4. Able to select Sensors for applications of Artificial Intelligence
5. Able to work on Real Time Hardware modules based on Industrial applications

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NARSAMPET, WARANGAL - 506332

Estd:2001

MACHINE LEARNING & ITS APPICATIONS

Eligibility:

The program is open to the all students of Electronics & Communication Engineering courses at undergraduate and post graduate level are welcome.

Registration Details:

- Students   100/-
- Research Scholars 200/-

How to Register:

The participants may log on to the website: www.jits.in and fill up the application form selecting the name of the course you may also send scanned copy of your filled application form Application format may be downloaded from the website(also available in this Boucher) . you can pay the fee through google/ phone pay: 9618336977

Address for Correspondence:
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Mobile: 9490377766.
Mobile: 7569839692.

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Sri T.V.R.N.Reddy - Joint-Secretary

Principal

Dr. M.Lokanadha Rao

Director

Prof.P.Rangaiah,

Convener:
Mr.Md.Hameed Pasha
Associate Professor &Head, ECE Department
Coordinator:
Mrs.S.Sandhya Rani
Assoc.Prof,ECE Department
Cell: 9030073969, 9849829390.

Schedule:

1. Last date of receipt of applications:
03rd March, 2018

2. Spot registrations allowed

The Applications in the prescribed form has to be reach coordinator either Email and/ or by hard copy.

One week WORKSHOP ON
" Machine Learning & Its Applications"
5th -9th March, 2018

Organized by:

DEPARTMENT OF Electronics and Communication Engineering

JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES

(U.G.C AUTONOUMOUS)

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Department of ECE

Department of Electronics and Communication Engineering has set vision "To be a premier department for education in Electronics and Communication Engineering in state of Telangana, moulding students into professional engineers". The department imparts state of the art education and encourages cutting edge research. It offers UG program, PG program in VLSI and ES. Well equipped Laboratories have been facilitated for the Students, research Scholars and the 50 Lakhs of funds generated from AICTE under various schemes like MODROBS, FDPs, STTPs. Received TEQIP-III fund from JNTU Hyderabad.

Established two laboratories namely Internet of Things(IoT) and Robotics sponsored by Texas Instruments USA.

About the Workshop:

Machine learning is a subfield of artificial intelligence, which is broadly defined as the capability of a machine to imitate intelligent human behavior. Artificial intelligence systems are used to perform complex tasks in a way that is similar to how humans solve problems. Signal processing may be used to prepare data for machine learning algorithms (such as cleaning up the signal and extracting features for voice recognition), machine learning can be used to solve some complex signal processing problems (such as using machine learning for voice activity detection.Resource Persons

Renowned academicians in this field are being invited to deliver lectures on specialized topics. Some of the eminent guest faculties in this field are:

List The following list of resource Person:

1. Prof.K.Kishan Rao, Professor, SNIST, Hyderabad
2. Dr.T.Kishore Kumar, Professor ECE, NIT Warangal.
3. Dr.Ghanshyam Singh, Professor of ECE, JITS
4. Prof.M.Sushanth Babu, Professor of EC E, JITS
5. Prof.A.Janardhan, Professor of ECE, JITS

One week WORKSHOP ON
" Machine Learning & Its Applications"
5th -9th March, 2018

REGISTRATION FORM

1. Name of Participant: _____
2. Roll No: _____
3. Institution: _____
4. Address for Correspondence: _____
_____
5. Mobile No: _____
6. Email: _____
7. Category: _____

DECLARATION

The information provided herewith is true to the best of my knowledge. I agree to abide by the rules and regulations governing the workshop programme.

Date:

Place: **Signature of the applicant**

Signature of the Sponsoring Authorities

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NARSAMPET, WARANGAL - 506332

Estd:2001

MACHINE LEARNING & ITS APPICATIONS

CONTENTS:

1. Linear Algebra Refresher
2. Probability Theory Refresher
3. Digital Signal Processing Refresher
4. Machine Learning basics
   a. Supervised and Unsupervised learning
   b. Classification and Regression (linear models)
   c. Evaluation metrics
5. Probability Models and Expectation Maximization Algorithm
   a. Gaussian Mixture Models
6. Neural Networks and Deep Learning
   a. Multi-class classification and Multi-label classification
   b. Different kinds of non-linearities, objective functions and learning methods
7. ML for Audio Classification
   a. Time Series Analysis, LSTMs and CNNs
8. ML for Speech Recognition
   a. Hidden Markov Models, Finite State Transducers and Dynamic Programming
9. ML for Music Information Retrieval
   a. Latent Variable Models, Matrix Factorization and Signal Separation
10. ML for Image Processing
    a. Transfer Learning, Attention models, Attribute-based learning
11. ML for Communication
    a. Deep learning for wireless applications

MACHINE LEARNING & ITS APPICATIONS

COURSE OUTCOMES

1. Remembering and understand the various machine learning methods.
2. Understand the Local models of machine learning concepts
3. Characterize the machine learning algorithms as supervised learning and unsupervised learning and Apply and analyze the various algorithms of supervised and unsupervised learning
4. Analyze the concept of neural networks for learning linear and non-linear activation functions
5 . Learn the concepts in Bayesian analysis from probability models and methods

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NARSAMPET, WARANGAL - 506332

Estd:2001

Internet of Things

Eligibility:
The program is open to the all students members of Electronics and Communication Engineering courses at undergraduate and post graduate level are welcome.

Registration Details:
IEEE /ISTE Students Free   Non IEEE
/ISTE Students   100/-

How to Register:
The participants may log on to the website: www.jits.in and fill up the application form selecting the name of the course you may also send scanned copy of your filled application form to Dr.Mastansheik.nglus2007@gmail.comor ecehod@jits.in. Application format may be downloaded from the website (also available in this brochure).

Address for Correspondence:
Head of the Department
Md. Hameed Pasha
Dept. of Electronics and Communication Engineering
Jayamukhi Institute of Technological Sciences, Narsampet, (Dist): Warangal-506332. (T.S)
Email: hodecejits@gmail

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Patron:
Sri Ch. Devender Reddy - Secretary
Sri T.V.R.N. Reddy - Joint-Secretary
Principal
Dr. M. Lokanadha Rao
Director
Prof. P. Rangaiah,
Head of Department
Md. Hameed Pasha

Convenor:

Dr.Ghanshyam Sing

Associate Professor & Head, Electronics & CommunicationEngineering

Coordinators:

Mrs.M.UshaRani

Assistant.Prof,Electronics & Communication EngineeringFacultyAdvisor
Cellno:9573825948.

Mrs.B.Saritha

Asst.Prof,Electronics&CommunicationEngineeringCe llno:9392262457.

Schedule: ,
Last date of receipt of applications:
          4thNovember, 2020

Spot registrations allowed
The Applications in the prescribed form has to be reach coordinator either Email and/ or by hard copy.
Programme sponsored by AICTE and many workshops, seminars and guest lectures for the benefits of Students.
About the Workshop:
     The Internet of Things (IoT) is expected to emerge as ahuge business opportunity for the Indian IT industry in

HANDS-ON WORKSHOP ON
"Internet Of Things (IoT)"
6th -10th November 2017

Organized by:

DEPARTMENT OF
Electronics and Communication Engineering
&
IETE STUDENT CHAPTER

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Tel (08718) 208586, Fax: (08718) 222569
Website: www.jits.in

from the historical city of Warangal and presents a picturesque and panoramic view. It has well developed laboratories in all disciplines, a Computer Centre with 1000 P-IV systems and with 2 IBM servers and associated software, a fully fledged training and Placement Cell, Educational Technology Cell, English Language Communication Skills Laboratory, a dedicated leased BSNL 1:1 8Mbps Internet Wi-Fi Connectivity spread over the entire campus. The institute has well qualified and experienced teaching faculty. The faculty is active in research and published more than 200 technical Research papers in reputed National and International Journals.
About the Department of Electronics and Communication Engineering
The Electronics and Communication Engineering (ECE) Department was started in the year 2001 and it is one of the well established and largest departments in the institution since the inception. The department offers Undergraduate (B. Tech ECE) and Postgraduate (M. Tech Embedded Systems) & (M. Tech VLSI Design) Programs that provide students with the knowledge and tools they need to succeed in the Electronics and Communication Engineering. Currently the department offers four year B.Tech course with student intake of 180. The department is accorded with NBA accreditation in the year 2009 and 2017 for 3 Years.The Department has grant in aids from various funding agencies for organizing FDPs, STTPs, MODROBs Schemes over 3 Years. The senior faculties of the department are executing TEQIP-III project under JNTU Hyderabad and also applied for DST projects.
Texas Instruments University program sponsored two research Laboratories on the Internet of Things and Robotics. Students of the department are well placed in MNCs and other government sectors.

theyearsahead.Accordingtoa Gatner Report,the globalIoTindustry would be worth USD 300 billion by 2020, withconnecteddevices increasingto 2.7 billionby   thatyear.
India, it is projected, will have a 5-6 percent share of thisindustry.Theexpansionof
IoTinthecountryisexpectedtobe spurred by several developments. The proliferation ofsensors, which will work with an ever growing number ofintelligent systems and applications. The IndianGovernment's plan of developing 100 smart cities in thecountry, for which Rs. 7,060 core has been allocated in thecurrent budget. The launch of the Digital India Program ofthe Government, which aims at transforming India into adigitallyempoweredsocietyandknowledgeeconomy.
Resource Persons
Renowned academicians in this field are being invited to deliver lectures on specialized topics. Some of the eminent guest faculties in this field are:
List of Resource Persons:

List of Resource Persons:
Dr.M.Krishnasamy
AssociateProfessor-
Electronics&communicationEngineering
Department,JITS,Narsampet,WGL.

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NARSAMPET, WARANGAL - 506332

Estd:2001

Internet of Things

List Of Contents

1. 1.Introduction to Internet of Things (IoT)
2. IoT Architecture
3. How to build an IoT product
4. Application of IoT technology , IoT Standards
5. Different requirements for implementation
6. Sensors & Interfacing
7. Software & Hardware platforms for IoT implementation
8. Major components in IoT implementation
9. Introduction to Arduino Hardware & IDE
10. Introduction to Cloud
11. Uploading sensor data to Cloud using API's
12. **Hardware, sensors, Embedded Systems**
13. Data Visualization, Data Analytics, Plugins, Import & Export,
14. Introduction to ESP8266 and NODE MCU firmware
15. Introduction to ESP8266 and NODE MCU firmware
16. Home automation using Voice Commands & Bluetooth
17. **Practicals Hands and Session**

Internet of Things

Course Outcomes.

1. Able to understand the application area of IOT.
2. Able to realize the revolution of Internet in Mobile Devices.
3. Able to Understand Clod & Sensor Networking.
4. Able to understand building blocks of Internet of Things.
5. Able to understand characteristics of IoT.

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NARSAMPET, WARANGAL - 506332

Estd:2001

RECENT ADVANCES IN NEURAL NETWORKS

Eligibility:
The program is open to the all students and Faculty members engaged in teaching Electronics and Communication Engineering courses at undergraduate and post graduate level are welcome.

TA, Boarding & Lodging Facilities:
All the participants will be provided working lunch, tea and snacks. No TA, Boarding and Lodging Facilities will be provided.

Registration Details:
Students   100/-

How to Register:
The participants may log on to the website:www.jits.in and fill up the application form selecting the name of the course you may also send scanned copy of your filled application form Application format may be downloaded from the website(also available in this brochure) . you can pay the fee through Google/ phone pay: 9885577504(verification details: Sukumar)

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Prof. P. Rangaiah,
Head of Department
Md. Hameed Pasha
Conveners:
Mr. Md. Hameed Pasha
Associate Professor & Head, Dept. of ECE
Coordinator:
Ms.S.Sandhya Rani
Assoc.Prof,ECE Department
Cellno: 9030073969,9849829390.
Mr. N.Sukumar
Asst.Prof, ECE Department
Cellno9885577504.

Schedule:
1.The Applications in the prescribed form after payment of fee has to reach the convener either by e-mail and/or by hard copy on or before 13th April, 2018.
2. Selected candidate will be informed by e-mail on 16th April 2018.
Programme sponsored by AICTE and many workshops, seminars and guest lectures for the benefits of Students and Faculty Members
About the Workshop:
The next industrial revolution is all about Artificial Intelligence, and it's happening now. soon it will change the world. This one day intensive workshop will broadly cover Manufacturing which have been maturing significantly over last decade and have now been identified by all major Defense, Power Generation, Aerospace, Agriculture and Automobile Agencies as key enabling technologies. The advantages are derived from the additional design capability gained that allows novel complex geometries with a little manufacturing cost. Hardware and components can be

One week WORKSHOP ON
"Recent Advances in Neural Networks"
16th Apr – 21st Apr, 2018

Organized by:

DEPARTMENT OF
Electronics and Communication Engineering
&
IETE STUDENT CHAPTER

JAYAMUKHI
INSTITUTE OF TECHNOLOGICAL SCIENCES
(U.G.C AUTONOUMOUS)
Permanently Affiliated to JNTUH, Accredited by NBA,
Narsampet, Warangal – 506332.
Tel (08718) 208586, Fax: (08718) 222569
Website: www.jits.in

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from 10-02-2009, which is a record in the history of technical institutions of T.S. Again three departments EEE, CSE, ECE was accredited by NBA for a period of three years. The institute is spread over an area of 40 acres of green pastures in Narsampet, about 30km away from the historical city of Warangal and presents a picturesque and panoramic view. It has well developed laboratories in all disciplines, a Computer Centre with 1000 P-IV systems and with 2 IBM servers and associated software, a fully fledged training and Placement Cell, Educational Technology Cell, English Language Communication Skills Laboratory, a dedicated leased BSNL 1:1 8Mbps Internet Wi-Fi Connectivity spread over the entire campus. The institute has well qualified and experienced teaching faculty. The faculty is active in research and published more than 200 technical Research papers in reputed National and International Journals.

About the Department of Electronics and Communication Engineering
The Electronics and Communication Engineering (ECE) Department was started in the year 2001 and it is one of the well established and largest departments in the institution since the inception. The department offers Undergraduate (B. Tech ECE) and Postgraduate (M. Tech Embedded Systems) & (M. Tech VLSI Design) Programs that provide students with the knowledge and tools they need to succeed in the Electronics and Communication Engineering. Currently the department offers four year B.Tech course with student intake of 180. The department is accorded with NBA accreditation in the year 2009 and 2017 for 3 Years. The Department has grant in aids from various funding agencies for organizing FDPs, STTPs, MODROBs Schemes over 3 Years. The senior faculties of the department are executing TEQIP-III project under JNTU Hyderabad and also applied for DST projects.
Texas Instruments University program sponsored two research Laboratories on the Internet of Things and Robotics. Students of the department are well placed in MNCs and other government sectors.

produced directly from the model with minimal tooling and reduced delivery times. Participants will be benefited with basic to advanced level development of technologies from the world renowned expert in this area.

Resource Persons
Renowned academicians in this field are being invited to deliver lectures on specialized topics. Some of the eminent guest faculties in this field are:
List of Resource Persons:

List of Resource Persons:
1. Prof.K.Kishan Rao, Professor, SNIST, Hyderabad
2. Dr.T.Kishore Kumar, Professor ECE, NIT Warangal
3. Dr.P.Krishna, Professor, of ECE, JITS
4. Dr.M.krishnaSamy, Professor of ECE,JITS
5. Prof.M.SushanthBabu, Professor of EC E, JITS
6. Prof.A.Janardhan, Professor of ECE, JITS
7. Ms.P.Soumya , Sr.Asst.Prof of ECE,SR

One week WORKSHOP ON
"Recent Advances in Neural Networks"
16th Apr – 21st Apr, 2018

REGISTRATION FORM

1. Name of Participant:

2 Qualification:

3. Department:

4. Institution/Industry:

5 Address
Institution/Industry:

6. Address for Correspondence:

7. Mobile No:

8. Email:

9. Category
(SAE/CC/Others):

DECLARATION
The information provided herewith is true to the best of my knowledge. I agree to abide by the rules and regulations governing the workshop programme.

Date:
Place: **Signature of the applicant**

Signature of the Sponsoring Authorities

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NARSAMPET, WARANGAL - 506332

Estd:2001

RECENT ADVANCES IN NEURAL NETWORKS

CONTENT:

1. Artificial Intelligence.
2. Historical Developments
3. Neuron Models
4. Neural Dynamics
5. Machine Learning basics
6. Perceptron models
7. Back Propogation Algorithm
8. Neural Networks and Deep Learning
9. Associative Memories - I
10. Kolmogorov theorem
11. Real time Applications

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NARSAMPET, WARANGAL - 506332

Estd:2001

RECENT ADVANCES IN NEURAL NETWORKS

COURSE OUTCOMES:

1. Understand the concepts of Neuron Models

2. Detail study on Neural Dynamics

3. Ability to understand the perceptron models

4. Design of various Memories which is used in real world applications

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Basics To Computer Data Applications

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NARSAMPET, WARANGAL - 506332

Estd:2001

Basics to Computer Data Applications

Course Content:

Computer Hardware
- Reading: Hardware
- Additional Reading: The PC
- Basic PC Parts
- Binary for Beginners
- An Overview of Storage Devices
- Computer Interfaces and Ports
- Hardware Jeopardy

Microsoft Excel
- Excel Learning Objectives
- Reading: Excel Trainings
- Assignment: Practice with Excel
- Course Project: Small Business Assignment Task 3

Microsoft Access
- Access Learning Objectives
- Access Training
- Assignment: Inventory Database
- Course Project: Small Business Assignment Task 4

Microsoft PowerPoint
- Powerpoint Learning Objectives
- Reading: PowerPoint Training
- Assignment: Virus Prevention Presentation
- Reading: Computer Basics Tutorial
- Videos: How My Computer Works
- Assignment: Defining the Computer Discussion
- Assignment: File Management
- Lecture: Computer Security
- Special Topics: Data Security and Privacy: Legal, Policy and Enterprise Issues
- Assignment: Computer Ethics
- Assignment: Microsoft Word
- Assignment: Microsoft Word Part II
- Assignment: Electronic Waste Research Paper
- Assignment: PowerPoint
- Assignment: How to Succeed Online PowerPoint
- Assignment: Creative Uses of Excel Discussion
- Assignment: Excel M&M Assignment
- Assignment: Budgeting with Excel
- Assignment: Four Personal Budgets in Excel

- Video: What is a Database?
- Assignment: Creating an Access Database
- Assignment: Planning for a Database

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Estd:2001

Course Outcomes:

1. Demonstrate a basic understanding of computer hardware and software.
2. Demonstrate problem-solving skills.
3. Apply logical skills to programming in a variety of languages.
4. Utilize web technologies.
5. Present conclusions effectively, orally, and in writing.

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NARSAMPET, WARANGAL - 506332

Estd:2001

Computer Training and Applications:

JAYAMUKHI
INSTITUTE OF TECHNOLOGICAL SCIENCES
UGC - AUTONOMOUS
Estd : 2001

A STUDENT PROGRAM
ON

COMPUTER TRAINING & APPLICATIONS

CSE: 07TH AUG TO 11TH AUG, 2017

CONDUCTED BY :

DEPARTMENT OF
COMPUTER SCIENCE AND ENGINEERING

VENUE:
JAYAMIKHI
INSTITUTE OF THECHNOLOGICAL SCIENCES
(UGC AUTONOMOUS)

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NARSAMPET, WARANGAL - 506332

Estd:2001

Computer Traning and Applications

List of Content:

1. Knowing computer

2. Operating Computer using GUI Based Operating System

3. Understanding Word Processing

4. Using Spread Sheet

5. Introduction to Internet, WWW and Web Browsers

6. Communications and collaboration

7. Making Small Presentation

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Estd:2001

Course Outcomes:

Upon successful completion of the program, you should have the skills to:

1. Understand the basic terminology of computers

2. Understand the practical concepts of MS Word, MS Excel, MS PowerPoint, and MS Access.

3. Work effectively with a range of current, standard, Office Productivity software applications.

4. Evaluate, select and use office productivity software appropriate to a given situation.

5. Apply basic adult learning and assessment principles in the design, development, and presentation of material produced by office productivity applications.

6. Demonstrate employability skills and a commitment to professionalism.

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NARSAMPET, WARANGAL - 506332

Estd:2001

Enhancement of SAP Software for civil Engineers.

Sponsoring & Forwarding

This is to certify that Prof./Dr./Sri./Ms. _____ working as _____ in the Department of _____ is sponsored to attend ONE WEEK WORKSHOP ON "ENHANCEMENT OF SAP SOFTWARE FOR CIVIL ENGINEERS 2018" Organized by Jayamukhi Institute of Technological Sciences, Narsampet, and Dist. Warangal from 20/08/2018 to 25/08/2018, In case the candidate is selected he/she shall be permitted to attend the course.

Date: Signature of the authorized person:
Name:
Designation:
(SEAL)

Registration Fee:
Registration fee of Rs.1000/- has to be paid along with registration form. It has to be paid in the form of crossed demand draft in favor of "**Principal, Jayamukhi Institute of Technological Sciences Narsampet**" payable at Warangal.

Schedule:
1. The Applications in the prescribed form after payment of fee has to reach the convenor either by e-mail and/or by hard copy on or before 18th August, 2018.
2. Selected candidate will be informed by e-mail on 19th August, 2018.

Address for Correspondence:
The Convenor,
ESSCE-2018
Department of Civil Engineering
Jayamukhi Institute of Technological Sciences,
Narsampet,(Dist): Warangal-506332. (T.S)
E-Mail: jitscivil@gmail.com
Mobile: 9490630014

About JITS Narsampet:
Jayamukhi Institute of Technological Sciences, Narsampet is sponsored by Jayamukhi Educational Society, Warangal. The Institute started in a humble way with Three B.Tech. Courses namely ECE, CSE and IT with an intake of 60 each in the year 2001. In a short span of 13 years it has developed as one of the premier Technical Institute in this part of the state. At present the institute offers 5 B.Tech Courses in ECE, CSE, EEE, ME, CIVIL and M.Tech courses in CSE, SWE, VLSI, PSCA and CAD/CAM in addition to MBA, Pharmacy, B.Ed, D.Ed courses under the fold of the same society. The Institute affiliated to JNTU, Hyderabad. Four B.Tech courses namely CSE, ECE, EEE & IT are accredited by NBA AICTE, New Delhi for period of 3 years with effect from 10-02-2009, which is a record in the history of technical institutions of T.S.

The institute is spread over an area of 40 acres of green pastures in Narsampet, about 30km away from the historical city of Warangal and presents a picturesque and panoramic view. It has well developed laboratories in all disciplines, a Computer Centre with 1000 P-IV systems and with 2 IBM servers and associated software, a fully fledged training and Placement Cell, Educational Technology Cell, English Language Communication Skills Laboratory, a dedicated leased BSNL 1:1 8 Mbps Internet Wi-Fi Connectivity spread over the entire campus. The institute has well qualified and experienced teaching faculty. The faculty is active in research and published more than 100 technical Research papers in reputed National and International Journals.

About CIVIL DEPARTMENT
The Department of Civil Engineering was established in the year 2013.The current intake in UG program is 120. The department has dedicated and qualified faculty as per the norms of AICTE. The department has equipped with sophisticated laboratories.

ONE WEEK WORKSHOP ON "ENHANCEMENT OF SAP SOFTWARE FOR CIVIL ENGINEERS 20th to 25th August-2018

Convenor
Mr.Gulam Samdani
Head, Dept.of Civil Engineering

Coordinator
Ms.ch.shalini
Asst. Prof –CED

Organized by

Estd :2001

DEPARTMENT OF CIVIL ENGINEERING
JAYAMUKHI
Institute of Technological Sciences
Narsampet, Warangal-506332 (T.S), INDIA.
Tel (08718) 208586, Fax: (08718) 222569
Website: www.jits.in

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NARSAMPET, WARANGAL - 506332

Estd:2001

Enhancement of SAP Software for civil Engineers.

List of Content:

- ❖ Introduction to Recent Advances in software and Importance of SAP Software of Construction Techniques

- ❖ Recent Advances in Software of Construction Methods

- ❖ Recent Advances in software of Construction Materials

- ❖ Introduction in software of Construction Management

- ❖ Importance of Construction Engineering in Recent Scenario of Indian Market.

- ❖ Foundation Engineering and its application to Construction Engineering.

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NARSAMPET, WARANGAL - 506332

Estd:2001

COURSE OUT COMES

➢ This workshop is students will get the hands-on with SAP Software. It is the only software which provides complete Structural details.

➢ This workshop aims the full range of Structural details, Construction processes, building services and Structural Drawings.

➢ The lecture focus on the real-time work-related concepts, issues, which is enhanced by the inputs of the instructor's practical experience.

➢ It Gives Knowledge to Civil Engineers, Entry or senior level, who wishes to streamline   their existing, knowledge to make a career in Structural planning for various building system.

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NARSAMPET, WARANGAL - 506332

Estd:2001

Enhancement of E-Tabs Software for civil Engineers.

Sponsoring & Forwarding

This is to certify that Prof./Dr./Sri./Ms. _____ working as _____ in the _____ Department of _____ is sponsored to attend ONE WEEK WORKSHOP ON "ENHANCEMENT OF E-TABS SOFTWARE FOR CIVIL ENGINEERS 2018" Organized by Jayamukhi Institute of Technological Sciences, Narsampet, and Dist. Warangal from 07/10/2018 to 12/10/2018, In case the candidate is selected he/she shall be permitted to attend the course.

Date:

Signature of the authorized person:

Name:
Designation:
(SEAL)

Registration Fee:
Registration fee of Rs.1000/- has to be paid along with registration form. It has to be paid in the form of crossed demand draft in favor of "Principal, Jayamukhi Institute of Technological Sciences Narsampet" payable at Warangal.

Schedule:
1. The Applications in the prescribed form after payment of fee has to reach the convenor either by e-mail and/or by hard copy on or before 04th October , 2018.
2. Selected candidate will be informed by e-mail on 05th October, 2018.

Address for Correspondence:
The Convenor,
ESSCE-2018
Department of Civil Engineering
Jayamukhi Institute of Technological Sciences,
Narsampet,(Dist): Warangal-506332. (T.S)
E-Mail: jitscivil@gmail.com
Mobile: 9490630014

About JITS Narsampet:
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ONE WEEK WORKSHOP ON
"ENHANCEMENT OF E-TABS SOFTWARE FOR CIVIL ENGINEERS
07th to 12th October-2018

Convenor
Mr.Gulam Samdani
Head, Dept.of Civil Engineering

Coordinator
Miss.Ch.Shalini
Asst. Prof–CED

Organized by

Estd :2001

DEPARTMENT OF CIVIL ENGINEERING
JAYAMUKHI
Institute of Technological Sciences
Narsampet, Warangal-506332 (T.S), INDIA.
Tel (08718) 208586, Fax: (08718) 222569
Website: www.jits.in

ABOUT THE COURSE:
This course titled " E-TABS Complete Course: From Beginner to Pro" teaches you ETABS is a highly efficient analysis and design program developed especially for building systems. It is loaded with an integrated system with an ability to handle the largest and most complex building models and configurations.

The lecture focus on the real-time work-related concepts, issues, which is enhanced by the inputs of the instructor's practical experience. It Gives Knowledge to Civil Engineers, Entry or senior level, who wishes to streamline their existing knowledge to make a career in Structural planning for various building system.

COURSE CONTENTS:

- Complete knowledge of E-TABS.
- The software has powerful CAD-like drawing tools with a graphical and object-based interface.
- It increases the productivity of structural engineers.
- The course will help the candidates to acquire in-depth details about the different procedures and simplified analysis aspects of the design models.
- Export the designed data to excel for further calculations.

Do preliminary design of structural members.

Eligibility:
The program is for the faculty of AICTE approved Engineering Colleges teaching and doing research in Civil Engineering.

TA, Boarding & Lodging Facilities:
No TA, Boarding and Lodging Facilities will be provided to the out station participants in the college hostels. Local participants will be provided working lunch, tea and snacks along with other participants.

Resource Persons:
Renowned academicians and researchers in this field are being invited to deliver lectures on specialized topics. Some of the eminent guest faculties in this field are:
1. Er.Shravan Kumar Project Manager, MS Tech IT Solutions

Chief Patron:
Sri Ch.Narsimha Reddy - President

Patron:
Sri Ch.Devender Reddy - Secretary
Sri T.V.R.N.Reddy - Joint-Secretary

Principal
Dr.M.Lokanadha Rao

Director
Prof.P.Rangaiah,

Convener:
Mr.Gulam Samdani– Head, CED

Coordinator:
Miss.Ch.Shalini -Asst.Prof

Organizing Committee:
1. Ms.M.SriKavya, Asst.Prof
2. Mr.S.Venkatesh, Asst.Prof
3. Mr.Syed Samiullah,Asst.Prof
4. Mr.DS Shiva Kumar, Asst.Prof
5. Ms.A.Sindhuja, Asst.Prof
6. Mr.YM Punith - Asst.Prof
7. Ms.B.HJoice, Asst.Prof
8. Ms.B.Laxmi, Asst.Prof.
9. Mr.A.ShivShankar, Asst.Prof
10. Ms.G.Swathi, Asst.Prof
11. Mr.L.Naveen, Asst.Prof

ONE WEEK WORKSHOP ON
"ENHANCEMENT OF E-TABS SOFTWARE FOR CIVIL ENGINEERS
07th to 12th October-2018

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Institute of Technological Sciences
Narsampet, Warangal-506332 (T.S), INDIA.
Tel (08718) 208586, Fax: (08718) 222569
Website: www.jits.in

1. Full Name: _____
2. Designation: _____
3. Institute: _____
4. Qualification: _____
5. Date of Birth: _____
6. Address for
   Correspondence: _____

Email: _____
Mobile: _____
Fee: 1000/-
Mode of Payment: _____

This is to certify that the information given by me is correct to the best of my knowledge.

Date: Signature:

JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES

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NARSAMPET, WARANGAL - 506332

Estd:2001

Enhancement of E-Tabs Software for civil Engineers.

List Of CONTENTS

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- ❖ Introduction in software of Construction Management

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NARSAMPET, WARANGAL - 506332

Estd:2001

COURSE OUT COMES

➢ This workshop is students will get the hands-on with E-Tabs Software. It is the only software which provides complete Structural details.

➢ This workshop aims the full range of Structural details, Construction processes, building services and Structural Drawings.

➢ The lecture focus on the real-time work-related concepts, issues, which is enhanced by the inputs of the instructor's practical experience.

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NARSAMPET, WARANGAL - 506332

Estd:2001

DNA SEQUENCE BY APPLYING DIGITAL IMAGE PROCESSING

Eligibility:
The program is open to the all students members in Electronics and Communication Engineering courses at undergraduate and post graduate level are welcome.

Registration Details:
IEEE /ISTE Students Free   Non IEEE
ISTE Students   100/-
Research Scholars   200/-

How to Register:
The participants may log on to the website:www.jits.in and fill up the application form selecting the name of the courseyou may also send scanned copy of your filled application form to gshyamsingh2@gmail.comor ecehod@jits.in Application format may be downloaded from the website(also available in this brochure) . you can pay the fee through **Google Pay/ phone pe:** 9490377766.

Address for Correspondence:
Head of the Department
Md.Hameed Pasha
Dept. of Electronics and Communication Engineering
Jayamukhi Institute of Technological Sciences,
Narsampet, (Dist): Warangal-506332. (T.S)
E-mail: hodecejits@gmail

About JITS Narsampet:
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Sri Ch.Narsimha Reddy - President

Patron:
Sri Ch.Devender Reddy - Secretary
Sri T.V.R.N.Reddy - Joint-Secretary

Principal
Dr.M.Lokanadha Rao

Director
Prof.P.Rangaiah,
Head of Department
Md.Hameed Pasha
Associate Professor, Electronics and Communication Engineering

Convener:

V.Madhu kumar
Associate Professor, Electronics and Communication Engineering

Coordinators:

Ms. Zulekha Tabassum
Asst. Professor, Electronics and Communication Engineering
IEEE /ISTE Faculty Advisor
SK.Mastan
Associate professor

ECE Dept

Cell no: 9949676900, 6281815605

Mr.M.Sridhar Asst.Prof, ECE Dept.
Ms.M.Ramya Asst.Prof, ECE Dept.
Mr. M. Sukumar Asst.Prof, ECE Dept.
Mr.Kalyan Asst.Prof, ECE Dept.
Mr. Rakesh Asst.Prof, ECE Dept.
Mr. Lalu Asst.Prof, ECE Dept.

Schedule:
1. Last date of receipt of applications: 22nd,October 2021
2. Spot registrations allowed

The Applications in the prescribed form has to be reach coordinator either email and/ or by hard copy.

This programme sponsored by AICTE and many workshops,

HANDS-ON WORKSHOP ON
"DNA SEQUENCE BY APPLYING
DIGITAL IMAGE PROCESSING "
23rd -28th October 2017

Organized by:

DEPARTMENT OF
Electronics and Communication Engineering
&
IETE STUDENT CHAPTER

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(U.G.C AUTONOUMOUS)
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Narsampet, Warangal – 506332.
Tel (08718) 208586, Fax: (08718) 222569
Website: www.jits.in

HANDS-ON WORKSHOP

on
" DNA Sequence by Applying Digital Image Processing "
23th -28th October 2017

Narsampet, about 30km away from the historical city of Warangal and presents a picturesque and panoramic view. It has well developed laboratories in all disciplines, a Computer Centre with 1000 P-IV systems and with2 IBM servers and associated software, a fully fledged training and Placement Cell, Educational Technology Cell, English Language Communication Skills Laboratory, a dedicated leased BSNL 1:1 8Mbps Internet Wi-Fi Connectivity spread over the entire campus. The institute has well qualified and experienced teaching faculty. The faculty is active in research and published more than 200 technical Research papers in reputed National and International Journals.

About the Department of Electronics and Communication Engineering
The Electronics and Communication Engineering (ECE) Department was started in the year 2001 and it is one of the well established and largest departments in the institution since the inception. The department offers Undergraduate (B. Tech ECE) and Postgraduate (M. Tech Embedded Systems) & (M. Tech VLSI Design) Programs that provide students with the knowledge and tools they need to succeed in the Electronics and Communication Engineering. Currently the department offers four year B.Tech course with student intake of 180. The department is accorded with NBA accreditation in the year 2009 and 2017 for 3 Years.The Department has grant in aids from various funding agencies for organizing FDPs, STTPs, MODROBs Schemes over 3 years. The senior faculties of the department are executing TEQIP-III project under JNTU Hyderabad and also applied for DST projects.

Texas Instruments University program sponsored two research Laboratories on the Internet of Things and Robotics. Students of the department are well placed in MNC's and other government sectors.

seminars and guest lectures for the benefits of Students.

About the Workshop:
Recently there are greater advances in bio-informatics and genomics signal processing. Digital Image Processing (DIP) applications in genomics sequence analysis have received great attention in recent years. New methods are being developed to analyze Deoxyribonucleic Acid(DNA) sequences. In order to use DSP principles to analyze DNA sequences, the DNA sequences should be converted into numeric sequences. Then the DSP algorithms are used in DNA analysis. Discrete Fourier Transform (DFT), digital filtering, Discrete wavelet transform (DWT), Parametric modeling and entropy are some of the important DSP concepts used for DNA analysis. In this paper, we present a review of the DIP techniques used for DNA analysis. We have explained the required basics behind molecular biology. We have discussed about the applications of DIP in sequence analysis.

Resource Persons
Renowned academicians in this field are being invited to deliver lectures on specialized topics. Some of the eminent guest faculties in this field are:
List of Resource Persons:

Mr. Dr. Ghanshyam Singh Professor of ECE, JITS
Prof. K. Kishan Rao, Professor, SNIST, Hyderabad
2. Dr.T. Kishore Kumar, Professor ECE, NIT Warangal.
3. Dr.P. Krishna, Professor of ECE, JITS
4. Dr.M. Krishna Samy, Professor of ECE,JITS
5. Prof. M. Sushanth Babu, Professor of EC E, JITS

REGISTRATION FORM

1. Name of Participant:...
2. Department:...
3. Institution:...
4. Address for Correspondence:...............................
....
5. Mobile No:..
6. Email:...
7. Category (SC/ST/Others):...................................

DECLARATION

The information provided herewith is true to the best of my knowledge. I agree to abide by the rules and regulations governing the workshop programme.

Date:

Place: **Signature of the applicant**

Signature of the Sponsoring Authorities

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NARSAMPET, WARANGAL - 506332

Estd:2001

DNA SEQUENCE BY APPLYING DIGITAL IMAGE PROCESSING

CONTENTS :

1) Introduction of DNA
2) Recent advances of DNA Sequences
3) Future Application of DNA Sequences
4) Studies of biological sequence data and sequence alignment
5) Software program me on 'DNA Invariant' by Microsoft visual basic 6.0 tools
6) Signal processing for DNA Sequences
7) Numeric Representation and Applications of DIP in the Analysis of DNA
8) Discrete Fourier Transform, Digital Filters
9) DSP of a sequence X64775 using AR model (b) DIP of same using FT
10) DSP on Bioinformatics new field of Genomic Signal Processing (GSP)
11) DIP Methods For Acceptors Splice Site Prediction
12) DIP tools that have been applied in analysis are addressed.
13) Application Of Digital Image Processing In DNA Sequence

DNA SEQUENCE BY APPLYING DIGITAL IMAGE PROCESSING

COURSE OUTCOMES:

CO1: Can able to remember basics of DNA Sequences

CO2: Understand Studies of biological sequence data and sequence alignment

CO3: Analyse various parameter of DNA Numeric Representation and Applications of DIP

CO4: Can able to design various Application Of Digital Image Processing In DNA Sequence

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Estd:2001

Applications of Power Electric Converter:

REGISTRATION FORM

1. Name of Participant: ....
2. Designation: ....
3. Qualification: ....
5. Department ....
6. Institution/Industry.............
7. Address for Correspondence............
8. Mobile No...................
9. Email............................
10. Category (ST/SC/OBC/OC/Others) ....

DECLARATION

The information provided herewith is true to the best of my knowledge. I agree to abide by the rules and regulations governing the workshop Program.

Date:
Place: Signature of the applicant

Registration Fee:
- Students   100/-

Address for Correspondence:
The Convenor,
Mr. D. Rajani Kumar
Department of Electrical and Electronics Engineering
Jayamukhi Institute of Technological Sciences,
Narsampet, (Dist): Warangal-506332. (T.S)
E-Mail: eeehod@jits.in

Chief Patron:
Sri Ch. Narsimha Reddy – President

Patron:
Sri Ch. Devender Reddy - Secretary
Sri T.V.R.N. Reddy - Joint-Secretary

Principal:
Dr.M. Lokanadha Rao

Director:
Prof.V. Rangaiah,

Conveners:-
Mr. D.Rajani Kumar
Associate Professor & Head, Dept. of EEE
Coordinator:
Mr.K.Sridevi, Asst.Prof
Dept. of EEE
Mobile: +91-8106956256

Mr.B. Nresh, Asst.Prof
Dept. of EEE
Mobile: +91-9133934491
E-Mail: eeehod@jits.in

How to Register:
The participants may log on to the website: www.jits.in and fill up the application form selecting the name of the course you may also send scanned copy of your filled application form to eeehod@jits.in on or before 6th April 2019.
Application format may be downloaded from the website (also available in this Broucher)

A One Week Workshop
On

" APPLICATIONS OF POWER ELECTRONICS CONVERTER "

12th -16th February 2018

Organized by
Department of Electrical and Electronics Engineering

JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES
(An Autonomous Institution)

(Permanently Affiliated to JNTUH, accredited by NBA, recognized Under UGC 2(f) & 12(b))

Narsampet, Warangal (Rural) – 506332(T.S)

About JITS Narsampet:

Jayamukhi Institute of Technological Sciences, Narsampet is sponsored by Jayamukhi Educational Society, Warangal. The Institute is approved by AICTE and permanently affiliated to JNTU, Hyderabad. The Institute started in a humble way with Three B.Tech. Courses namely ECE, CSE and IT with an intake of 60 each in the year 2001. In a short span of 19 years, it has developed as one of the premier Technical Institute in this part of the Telangana state. At present the institute offers 5 B. Tech Courses in ECE, CSE, EEE, ME, CIVIL and M. Tech courses in CSE, SWE, VLSI, Embedded Systems, PSCA and CAD/CAM in addition to MBA, Pharmacy, B. Ed, D. Ed courses under the fold of the same society. B. Tech courses namely EEE& ECE are accredited by NBA, New Delhi. The institution is recognized under UGC 2(f) & 12(b) and also got an AUTONOMOUS state by the UGC among technical institutions of Telangana State in this part of the region.

The institute is spread over an area of 40 acres of green pastures in Narsampet, about 30km away from the historical city of Warangal and presents a picturesque and panoramic view. It has well developed laboratories in all disciplines, a Computer Centre with 1000 latest computers with 3 IBM servers and associated software, a fully fledged training and Placement Cell, Educational Technology Cell, English Language Communication Skills Laboratory, a dedicated leased BSNL 100 Mbps Internet Wi-Fi Connectivity spread over the entire campus. The institute has well qualified and experienced teaching faculty. The faculty is active in research and published more than 300 technical Research papers in reputed National and International Journals.

About EEE Department:

The Department of Electrical & Electronics Engineering was established in the year 2002.The current intake in UG program is 120 and PG program M.Tech (PSCA) is 24.The department is Accredited by NBA, New Delhi. The department has dedicated and qualified faculty as per the norms of AICTE.The department has equipped with sophisticated laboratories. The department had already organized two AICTE sponsored two week FDP program entitled "Soft Computing Techniques in Electrical Engineering" in the year 2010 with a grant of 7 Lakh and another FDP entitled "Renewable Energy Sources – Integration to Power Systems" in the year 2013 with a grant of 4 Lakh .

> In this workshop first high power electronic devices were made using mercury-arc valves. In modern systems, the conversion is performed with semiconductor switching devices such as diodes, thyristors, and power transistors such as the power MOSFET and IGBT.
> An AC/DC converter (rectifier) is the most typical power electronics device found in many consumer electronic devices, e.g. television sets, personal computers, battery chargers, etc.

Resource Persons

Renowned academicians in this field are being invited to deliver lectures on specialized topics. Some of the eminent guest faculties in this field are:

List of Resource Persons:
1. Venkata Sarath Panathula
   Technical Trainer/Consultant
   Enflare Technologies, Nellore
2. Dr.M.Mithun Baskar
   Professor – Electrical and Electronics
   Engineering Department. JITS, WGL

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Estd:2001

Applications of Power Electric Converter:

List Of Contents:

1. Introduction to Converter

2. Recent Advances of Electric Converter

3. Controllers

4. Types of Converter

5. AC to DC conversion (Rectification)

6. Explanation of AC voltage controller

7. Feature application of Electric Converter

8. Outcomes

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Course Outcomes:

➢ Power electronics is a key technology for energy saving and high fictionalizing in energy usage.

➢ The power electronics application has a tendency toward high voltage and lessening current to reduce conduction loss.

➢ The key component for high voltage power electronics is a power semiconductor device for the switching operation.

➢ Diamond is a prospective semiconductor material for a power device.

Advanced Technology in IC Engine

Day 01-CONVERGE Studio Module

In this module, you will learn how to set up a CFD simulation using CONVERGE CFD. You will be provided with step-by-step instructions on how to do the following:

- o CAD import and cleanup
- o Decomposing the model into boundaries and volumetric regions
- o Inputting valve timing
- o Choosing turbulence and combustion models
- o Running the case in a parallel environment
- o Post-processing

Day02-Surface Preparation

- In this module, you will learn surface preparation in complete detail. Specifically,
    - o Setting up the piston motion profile
    - o Boundary flagging
    - o Setting up the intake and exhaust valves

Day03-Region Initialization and Valve Motion Setup

- In this module, you will learn the following concepts:
    - o Initializing pressure, temperature, and species concentration in the intake, exhaust, and fire deck regions.
    - o Disconnect triangles
    - o Valve timing and the concept of minimum lift
    - o Valve profile input

Day04-Turbulence Modelling

- In this module, you will learn how turbulence is modelled and simulated in a state-of-the-art CFD solver. You will learn about the different classes of turbulence models and understand their merits and demerits. In this module, you will also learn about,
    - o RANS approach to model turbulence
    - o The math involved behind RANS and its derivations

- Learn the theory behind the various types of turbulence models available (RNG k-, k-SST, and so on) and recognize which model is appropriate for which type of application.

Day05-Combustion Modelling

- In this module, you will learn how the SAGE detailed chemical kinetics solver works.
- In addition to this, you will also learn how to use the Shell CTC combustion model.

Day06-Emissions Modelling

- To design efficient engines, one needs to have a firm grasp of emissions modelling.

You will learn about the Hiroyasu soot model and the Zeldovich nox model and apply them in engine simulations.

Course Outcomes

1. The students will learn the application of Computational Fluid Dynamics (CFD) theory on internal flows with a special focus on simulating the Engines (S.I Engines and C.I Engines).
2. Working on CONVERGE STUDIO will help the students study the inside workings of an engine, from fuel injection, compression, spark ignition to combustion and flame propagation
3. CONVERGE STUDIO has an added advantage of automated meshing.
4. The students will be able to set up a 4 stroke port fuel injection Internal Combustion Engine using CONVERGE CFD
5. Learn the theory of turbulence modeling, spray modeling, source, and sink
6. Learn to setup Conjugate Heat Transfer (CHT) simulation

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Hands on Workshop about New Trends in Mechanical Engineering:

List of Content:

1.4D Printing Technology

2.Flywheel Energy Storage

3. Friction Stir Welding

4.Industry 4.0(Fourth Industrial Revolution)

5.Metal 3D Printing

Course Outcomes:

1. Understand the Concept on 3D and 4D Printing Technology

2. Ability to learn and acquire knowledge on Flywheel energy Storage

3. Revising and retrieving the concepts on Industrial revaluation and implementing concepts for Fourth Industrial Revolution.

4. Ability to understand Friction Stir welding

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Sales Force Fundamentals:

3/3/22, 5:45 PM   Jayamukhi Educationalk Society Mail - Salesforce Fundamentals Women in Tech Initiative

M Gmail   Manoj Kumar Myakala <tpo@jits.in>

Salesforce Fundamentals Women in Tech Initiative

Ramakrushan Mamidi <rm4-task@telangana.gov.in>   Wed, Mar 24, 2021 at 8:53 PM
To: BITS ENG PRINCIPAL <principal@bitswgl.ac.in>, CITS ENG PRINCIPAL <principalcits@chaitanyacolleges.com>, CJIT ENG PRINICPAL <principal.cjits@gmail.com>, GANA ENG PRINCIPAL <gecprincipal@yahoo.co.in>, JAYA ENG PRINCIPAL <principal@jits.in>, KITS ENG PRINCIPAL1 <principal.kitswgl@gmail.com>, KU ENG PRINCIPAL <principal.kucet@gmail.com>, SR ENG PRINCIPAL <principal@srecwarangal.ac.in>, SUM ENG PRINCIPAL <principalsritw@gmail.com>, SVS ENG PRINCIPAL <principal@svsit.ac.in>, TALL ENG PRINCIAPAL1 <principal.tpce@gmail.com>, UNI ENG PRINCIPAL <principalucetw@gmail.com>, "UNI1, PRIN" <drstadisetty@gmail.com>, VCE ENG PRINCIPAL <principal.vce@gmail.com>, VEC ENG PRINCIPAL <principal.vec@gmail.com>, WITS ENG PRINCIPAL <principal.wits@gmail.com>, "tpce, director" <director.tpce@gmail.com>
Cc: BITS ENG TPO <bits.warangal@gmail.com>, CITS ENG TPO <citstnp@chaitanyacolleges.com>, CJITS ENG TPO <tpocjits@gmail.com>, GAN ENG TPO <tpo.ganapathy@gmail.com>, JITS ENG TPO <jitstpo@gmail.com>, JITS ENG TPO1 <tpo@jits.in>, KITS ENG TPO <task.kitsw@gmail.com>, KU ENG TPO <placement.kucet@gmail.com>, SR ENG TPO <tpo@srecwarangal.ac.in>, SR ENG TPO1 <srectpo@gmail.com>, SUM ENG TPO <sritw.tpo@gmail.com>, SVS ENG TPO <placements@svsit.ac.in>, TALL ENG TPO <tpce.task@gmail.com>, UNI ENG TPO <kiranbollam22@gmail.com>, VAAG ENG TPO <vaagdevi.tpo@gmail.com>, VCE ENG TPO <ssvn.sarma1@gmail.com>, WITS ENG TPO <witstpo@gmail.com>

Dear Principal/TPO,

TASK and Salesforce are bringing up a women centric program called **Salesforce Fundamentals- Women in Tech Initiative**.

Many of Salesforce hiring partners and customers are looking forward to hire talented women to join their organisations as part of their Diversity and Inclusion initiative. They are looking to skill and empower 1K girl students.
The enrolled students will go through a series of curated trailmixes and virtual trainings taken by instructors over the weekends.

Please find the attachment for the complete details about the program.

Kindly note that this program is **only for TASK registered girl students.**

Eligible students for this initiative is as below :

B.Tech / BE Colleges - 2nd year Girl Students of CSE, IT, ECE, EEE, Mechanical streams

Degree Colleges - 2nd year Girl Students of Bcom, BSc, BBA

MBA Colleges - 1st year Girl Students

Please see below the key highlights.

Key Highlights:

- Launch date - April, 2021
- Duration: 12 weeks
- Time commitment from students: 4-6 hours a week
- Format: Self paced with guided help from instructors
- Mentorship and Job Connections

I request you to give yours SPOCS and faculty nominations by **29th March, 2021:**

https://mail.google.com/mail/u/1/?ik=8623e2b3af&view=pt&search=all&permmsgid=msg-f%3A1695127345015052278&simpl=msg-f%3A1695127   1/2

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Estd:2001

Sales Force Fundamentals:

List of content:

Get Start with sale force Platform

Discover Use cases for platform

Understand sale force architecture

Data Modeling

Create Object Relationship

Work with schema builder

Data Management

Import Data

Export data

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Course Outcomes:

1. Ability to Understand the Concept of Sales force fundamentals

2. Understand the Cases studies of Sales force Platform and applying the logic for program development

3. Implementation and design Sales force Platform with data modeling

4. Ability to Understand the concepts of Data Base management system and applying to Project development