JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES
(An Autonomous Institution)

The following list of Value Added courses organized by the Institution with Brochure,contents and outcomes for the academic year 2019-20

S.NO	NAME OF THE VALUE ADDED COURSE
1	Artificial Intelligence In Robotics
2	Machine Learning for signal Processing
3	Data Acquisition System
4	Power System Stability Implement by Using Fact Devices
5	Ethical Hacking & Cyber Security
6	Android Application Development
7	Enhancement of SAP Software for Civil Engineers
8	Enhancement of E-LABs Software for Civil Engineers
9	Robotics
10	Design & Fabrication of PCB
11	I Week Workshop on Robotics and Its Applications
12	Brain wave Robotics & its Applications
13	R.F. Design Using Latest CMOS Techniques
14	Java Fundamental and Data Base Management Training

ARTIFICIAL INTELLIGENCE IN ROBOTICS

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NARSAMPET, WARANGAL - 506332

Estd:2001

ARTIFICIAL INTELLIGENCE IN ROBOTICS

CONTENTS:

1. Artificial intelligence.
2. Data science.
3. Computer science.
4. Mathematics.
5. Statistics.
6. Data mining.
7. Deep learning.
8. Natural Language Processing.

ARTIFICIAL INTELLIGENCE IN ROBOTICS

COURSE OUTCOMES

1. Differentiate between a rudimentary problem and AI problem, it's Characteristics and problem solving Techniques.
2. Determine and evaluate the various search strategies.
3. Compare and contrast the various " knowledge representation" schemes of AI.
4. Understand and analyze the various reasoning techniques involved in solving AI problems.
5. Understand the different learning techniques, apply the AI techniques to solve the real world problems.

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NARSAMPET, WARANGAL - 506332

Estd:2001

MACHINE LEARNING FOR SIGNAL PROCESSING

Eligibility:

The program is open to the all students members of ECE Engineering courses at undergraduate and post graduate level are welcome.

Registration Details:

- Students 100/-
- Research Scholars 200/-

How to Register:

The participants may log on to the website www.jits.in and fill up the application form selecting the name of the course you may also send scanned copy of your filled application form. Application format may be downloaded from the website(also available in this brochure) , you can pay the fee through Google/ phone pay: 9618536977(verification details: Nethavath Lalu)

Address for Correspondence:
The Convener,
Head of the Department
Dept of ECE
Jayamukhi Institute of Technological Sciences, Narsampet, (Dist). Warangal- 506332. (T.S)
E-mail: hameedjits@gmail.com
Mobile: 9490377766.
Mobile: 7569839692.

Chief Patron:

Sri Ch. Narsimha Reddy - President

Patron:

Sri Ch. Devender Reddy - Secretary

Sri T.V.R.N. Reddy – Joint-Secretary

Principal

Dr. M. Lokanadha Rao

Director

Prof. P. Rangaiah.

Convener:
Mr. Md. Hameed Pasha
Associate Professor &Head, Electronics and communication Engineering

Coordinators:
Dr. Ghanshyam Sing
Prof. ECE Department
Cell: 6281815605
Mr. N. Lalu
Asst. Prof. ECE Department
Cell:9618536977

Schedule:

1. Last date of receipt of applications:
   19th October, 2019

2. Spot registrations allowed

   The Applications in the prescribed form has to be reach coordinator either Email and/ or by hard copy.

ONE WEEK WORKSHOP ON "MACHINE LEARNING FOR SIGNAL PROCESSING"
21st -25th October, 2019

Organized by:

DEPARTMENT OF
Electronics and Communication Engineering

JAYAMUKHI
INSTITUTE OF TECHNOLOGICAL SCIENCES
(U.G.C AUTONOUMOUS)
Permanently Affiliated to JNTUH, Accredited by NBA.
Narsampet, Warangal – 506332.
Tel (08718) 308586, Fax: (08718) 222569
Website: www.jits.in

About JITS Narsampet

Jayamukhi Institute of Technological Sciences, Narsampet Sponsored by the Jayamukhi Educational Society, the Jayamukhi Institute of Technological Sciences came into being in 2001 to provide quality and contemporary education with social relevance in the engineering faculty with an ultimate vision to maintain global standards in higher learning and research. The Institute has the approval of AICTE and recognized by the Government of Telangana. It is permanently affiliated to Jawaharlal Nehru Technological University (JNTU), Hyderabad, UGC Autonomous & accredited by NBA.

The Institute has come upon 40 acres of green pastures in Narsampet, about 30km away from the historic city of Warangal and presents a picturesque and panoramic view. JITS offers a four-year B.Tech Programme in the disciplines of CSE, ECE, EEE,CIVIL and ME

Department of ECE

Department of Electronics and Communication Engineering has set vision "To be a premier department for education in Electronics and Communication Engineering in state of Telangana, moulding students into professional engineers". The department imparts state of the art education and encourages cutting edge research. It offers UG program, PG programs in VLSI and ES. Well equipped Laboratories have been facilitated for the Students, research Scholars and the 50Lakhs of funds generated from AICTE under various schemes like MODROBS, FDPs, STTPs. Received TEQIP-III fund from JNTU Hyderabad.

Established two laboratories namely Internet of Things (IoT) and Robotics sponsored by Texas Instruments USA.

About the Workshop:

Machine learning is a sub-field of artificial intelligence, which is broadly defined as the capability of a machine to imitate intelligent human behavior. Artificial intelligence systems are used to perform complex tasks in a way that is similar to how humans solve problems. Signal processing may be used to prepare data for machine learning algorithms (such as cleaning up the signal and extracting features for voice recognition), machine learning can be used to solve some complex signal processing problems (such as using machine learning for voice activity detection.Resource Persons

Renowned academicians in this field are being invited to deliver lectures on specialized topics. Some of the eminent guest faculties in this field are:

List The following list of resource Person:

1. Prof. K. Kishan Rao, Professor, SNIST, Hyderabad

2. Dr. M. Krishna Samy, Professor of ECE,JITS

3. Prof. A. Janardhan, Professor of ECE, JITS

4. Dr. S. Sandhya Rani, Professor of ECE.JITS

ONE WEEK WORKSHOP ON "MACHINE LEARNING FOR SIGNAL PROCESSING"
21st -25th October, 2019

REGISTRATION FORM

1. Name of Participant ....
2. Department ....
3. Institution ....
4. Address for Correspondence ....
   ....
   ....
5. Mobile No: ....
6. Email: ....
7. Category (SC/ST/Others) ....

DECLARATION

The information provided herewith is true to the best of my knowledge. I agree to abide by the rules and regulations governing the workshop programme.

Date:

Place:

Signature of the applicant

Signature of the Sponsoring Authorities

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NARSAMPET, WARANGAL - 506332

Estd:2001

MACHINE LEARNING FOR SIGNAL PROCESSING

CONTENTS:

1. Linear Algebra Refresher
2. Probability Theory Refresher
3. Digital Signal Processing Refresher
4. Machine Learning basics
   a. Supervised and Unsupervised learning
   b. Classification and Regression (linear models)
   c. Evaluation metrics
5. Probability Models and Expectation Maximization Algorithm
   a. Gaussian Mixture Models
6. Neural Networks and Deep Learning
   a. Multi-class classification and Multi-label classification
   b. Different kinds of non-linearities, objective functions and learning methods
7. ML for Audio Classification
   a. Time Series Analysis, LSTMs and CNNs
8. ML for Speech Recognition
   a. Hidden Markov Models, Finite State Transducers and Dynamic Programming
9. ML for Music Information Retrieval
   a. Latent Variable Models, Matrix Factorization and Signal Separation
10. ML for Image Processing
    a. Transfer Learning, Attention models, Attribute-based learning
11. ML for Communication
    a. Deep learning for wireless applications

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NARSAMPET, WARANGAL - 506332

Estd:2001

MACHINE LEARNING FOR SIGNAL PROCESSING

COURSE OUTCOMES

1.Remembering and understand the various machine learning methods.

2. Understand the Local models of machine learning concepts

3. Characterize the machine learning algorithms as supervised learning and   unsupervised learning and Apply and analyze the various algorithms of supervised and unsupervised learning

4. Analyze the concept of neural networks for learning linear and non-linear activation functions

5. Learn the concepts in Bayesian analysis from probability models and methods

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NARSAMPET, WARANGAL - 506332

Estd:2001

DATA ACQUISITION SYSTEM

HANDS-ON WORKSHOP ON "DATA ACQUISITION SYSTEM"
13th – 18th April 2020

Organized by:

DEPARTMENT OF
Electronics and Communication Engineering
&
IETE STUDENT CHAPTER

JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES
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Permanently Affiliated to JNTUH, Accredited by NBA.
Narsampet, Warangal – 506332.
Tel (08718) 208588, Fax: (08718) 222588
Website: www.jits.in

HANDS-ON WORKSHOP ON "DATA ACQUISITION SYSTEM"
13th – 18th April 2020

REGISTRATION FORM

1. Name of Participant:
2. Department:
3. Institution:
4. Address for Correspondence:
5. Mobile No:
6. Email:
7. Category (SC/ST/Others):

DECLARATION
The information provided herewith is true to the best of my knowledge. I agree to abide by the rules and regulations governing the workshop programme.

Date:
Place:

Signature of the applicant

Signature of the Sponsoring Authorities

DATA ACQUISITION SYSTEM

CONTENTS LIST:

1.Introduction to Data Acquisition system

2. Digital to Analog and Analog to Digital conversion

3. Data acquisition and manipulation

4.Op amp applications & frequency Response

5.A/D s in microcontroller

6.Data sampling and recording

7.A/D s in MC9S12DPS56B

8.68HC11 Analog I/O

9.Instrumentation and product Testing

10.Applications of Data acquisition system

DATA ACQUISITION SYSTEM

COURSE OUTCOMES:

1. Identify digital control and data acquisition system components.
2. Determine theOPamp applications and frequency Response
3. Determine the Data acquisition and manipulation
4. Select A/D s in microcontroller
5. Design and test Data acquisition system circuit

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NARSAMPET, WARANGAL - 506332

Estd:2001

A Workshop on Power System Stability Implement by Using Fact Devices

Brochure:

REGISTRATION FORM

1. Name of Participant: ....
2. Designation: ....
3. Qualification: ....
5. Department ....
6. Institution/Industry...........
7. Address for Correspondence...........
8. Mobile No ....
9. Email.......................
10. Category (ST/SC/OBC/OC/Others) ....

DECLARATION

The information provided herewith is true to the best of my knowledge. I agree to abide by the rules and regulations governing the workshop Program.

Date:
Place: **Signature of the applicant**

Registration Fee:
- Students   100/-

Address for Correspondence:
The Convenor.
Mr. D. Rajani Kumar
Department of Electrical and Electronics Engineering
Jayamukhi Institute of Technological Sciences,
Narsampet, (Disc): Warangal-506332. (T.S)
E-Mail: eeehod@jits.in

Chief Patron:
Sri Ch. Narsimha Reddy – President

Patron:
Sri Ch. Devender Reddy - Secretary
Sri T.V.R.N. Reddy - Joint-Secretary

Principal:
Dr.M. Lokanadha Rao

Director:
Prof.V. Rangaiah.

Conveners:
Mr. D.Rajani Kumar
Associate Professor & Head, Dept. of EEE
Coordinator:
Mr.N.Anil, Asst.Prof –
Dept. of EEE
Mobile: +91-9908117369

Mr.G.Nagesh, Asst.Prof
Dept. of EEE
Mobile: +91-7569909980
E-Mail: eeehod@jits.in

How to Register:
The participants may log on to the website: www.jits.in and fill up the application form selecting the name of the course you may also send scanned copy of your filled application form to eeehod@jits.in or Mr. nirrmakaniianil9@gmail.com on or before 6th April 2019.
Application format may be downloaded from the website (also available in this Broucher)

A One Week Workshop
On

" POWER SYSTEM STABILITY IMPROVEMENT BY USING FACTS DEVICES "

19th -23rd November 2019

Organized by
Department of Electrical and Electronics Engineering

JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES
(An Autonomous Institution)
(Permanently Affiliated to JNTUH, accredited by NBA, recognized Under UGC 2(f) & 12(b))

Narsampet, Warangal (Rural) – 506332(T.S)
@ jayamukhi.ac.in

About JITS Narsampet:
Jayamukhi Institute of Technological Sciences, Narsampet is sponsored by Jayanmukhi Educational Society, Warangal. The Institute is approved by AICTE and permanently affiliated to JNTU, Hyderabad. The Institute started in a humble way with Three B.Tech Courses namely ECE, CSE and IT with an intake of 60 each in the year 2001. In a short span of 19 years, it has developed as one of the premier Technical Institute in this part of the Telangana state. At present the institute offers 5 B. Tech Courses in ECE, CSE, EEE, ME, CIVIL and M. Tech courses in CSE, SWE, VLSI, Embedded Systems, PSCA and CAD/CAM in addition to MBA, Pharmacy, B. Ed, D. Ed courses under the fold of the same society. B. Tech courses namely EEE& ECE are accredited by NBA, New Delhi. The institution is recognized under UGC 2(f) & 12(b) and also got an AUTONOMOUS state by the UGC among technical institutions of Telangana State in this part of the region.

The institute is spread over an area of 40 acres of green pastures in Narsampet, about 30km away from the historical city of Warangal and presents a picturesque and panoramic view. It has well developed laboratories in all disciplines, a Computer Centre with 1000 latest computers with 3 IBM servers and associated software, a fully fledged training and Placement Cell, Educational Technology Cell, English Language Communication Skills Laboratory, a dedicated leased BSNL 100 Mbps Internet Wi-Fi Connectivity spread over the entire campus. The institute has well qualified and experienced teaching faculty. The faculty is active in research and published more than 300 technical Research papers in reputed National and International Journals.

About E.E.E. Department:

The Department of Electrical & Electronics Engineering was established in the year 2002.The current intake in UG program is 120 and PG program M.Tech (PSCA) is 24.The department is Accredited by NBA, New Delhi. The department has dedicated and qualified faculty as per the norms of AICTE.The department has equipped with sophisticated laboratories. The department had already organized two AICTE sponsored two week FDP program entitled "Soft Computing Techniques in Electrical Engineering" in the year 2010 with a grant of 7 Lakh and another FDP entitled "Renowable Energy Sources . Integration to Power Systems" in the year 2013 with a grant of 4 Lakh .

About the Workshop:
- Power system stability is defined as the property of a power system that enables it to remain in a state of operating equilibrium under normal operating conditions and to regain an acceptable state of equilibrium after being subjected to a disturbance.
- Flexible and dynamic control of power systems are allowed through Flexible AC transmission system or FACTS. The investigation of FACTS devices for the enhancement of system stability has been done.

Resource Persons
Renowned academicians in this field are being invited to deliver lectures on specialized topics. Some of the eminent guest faculties in this field are:

List of Resource Persons:
1. Dr. P.Sadanandam
   Professor – Electrical and Electronics Engineering Department. VCE, WGL
2. Dr.A.Naraina
   Professor – Electrical and Electronics Engineering Department. JITS, WGL

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NARSAMPET, WARANGAL - 506332

Estd:2001

A Workshop on Power System Stability Implement by Using Fact Devices

Contents:

1. Introduction to Power System Stability Improvement By Using Facts Devices

2. Static power-electronic devices

3. AC transmission networks

4. Power transfer capability of transmission line

5. More Effective Use of Transmission Corridors and Increased Loading

6. Power quality improvement

7. Future advantages of power system stability improvement

8. Conclusion

A Workshop on Power System Stability Implement by Using Fact Devices

Outcomes:

- ➤ By the use of FACTS devices high power transfer capacity is increased at the same time line tripling faults are also reduces.
- ➤ New industries want good quality of electric supply.
- ➤ Constant voltage with less fluctuation and desired frequency as mentioned by electricity department
- ➤ These devices are also employed for congestion management and loss optimization.

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NARSAMPET, WARANGAL - 506332

Estd:2001

Ethical Hacking & Cyber Security

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NARSAMPET, WARANGAL - 506332

Estd:2001

Ethical Hacking & Cyber Security

Course Content:

➢ **Introduction:**Hacking Impacts, The Hacker Framework: Planning the test, Sound Operations, Reconnaissance, Enumeration,

➢ Vulnerability Analysis, Exploitation,

➢ Final Analysis, Deliverable, Integration Information Security Models:

➢ Computer Security, Network Security, Service Security,

➢ Application Security, Security Architecture Information Security Program:

➢ **The Business Perspective:** Business Objectives, Security Policy, Previous Test Results, Business Challenges Planning for a Controlled Attack: Inherent Limitations, Imposed Limitations,

➢ Timing is Everything, Attack Type, Source Point, Required Knowledge, Multi-Phased Attacks, Teaming and Attack Structure, Engagement Planner,

➢ The Right Security Consultant, The Tester, Logistics, Intermediates, Law Enforcement

➢ Preparing for a Hack:Technical Preparation, Managing the Engagement Reconnaissance: Social Engineering, Physical Security, Internet Reconnaissance

Ethical Hacking & Cyber Security

<u>Course Outcomes</u>:

1. Gain the knowledge of the use and availability of tools to support an ethical hack
2. Gain the knowledge of interpreting the results of a controlled attack
3. Understand the role of politics, inherent and imposed limitations and metrics for planning of a test
4. Comprehend the dangers associated with penetration testing

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Estd:2001

Android   Application Development

Android   Application Development

Course Content

- o Basics of Android?
- o Importance and scope
- o Android Architecture
- o Android Stack
- o Android Applications Structure
- o Android Emulator
- o Android SDK
- o Overview of Android Studio
- o Android and File Structure
- o Android Virtual Device Manager
- o DDMS
- o LogCat
- o Eclipse and SDK installation
- o AVD creation
- o Project Structure
- o Creating a Project
- o Working with the AndroidManifest.xml
- o Using the log system Activities
- o Application context
- o Intents
- o Activity life cycle
- o Supporting multiple screen sizes

Android   Application Development

Course Outcomes:

1. Build enterprise level mobile applications with Kotlin on Android

2. Understand both the basic and advanced concepts of Kotlin

3. Understand why use Kotlin over Java

4. Install and configure Android Studio

5. Explain and use key Android programming concepts

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NARSAMPET, WARANGAL - 506332

Estd:2001

Enhancement of SAP Software for Civil Engineer

Sponsoring & Forwarding

This is to certify that Prof /Dr /Sri /Ms _____ working as _____ in the _____ Department of _____ is sponsored to attend ONE WEEK WORKSHOP ON "ENHANCEMENT OF E-TABS SOFTWARE FOR CIVIL ENGINEERS 2018" Organized by Jayamukhi Institute of Technological Sciences, Narsampet, and Dist. Warangal from 26/08/2019 to 31/08/2019. In case the candidate is selected he/she shall be permitted to attend the course.

Date: _____   Signature of the authorized person:

Name:
Designation:
(SEAL)

Registration Fee:
Registration fee of Rs.1000/- has to be paid along with registration form. It has to be paid in the form of crossed demand draft in favor of "Principal, Jayamukhi Institute of Technological Sciences Narsampet" payable at Warangal.

Schedule:
1. The Applications in the prescribed form after payment of fee has to reach the convenor either by e-mail and/or by hard copy on or before 04th October , 2018.
2. Selected candidate will be informed by e-mail on 17th August, 2018.

Address for Correspondence:
The Convenor,
ESSCE-2017
Department of Civil Engineering
Jayamukhi Institute of Technological Sciences,
Narsampet,(Dist): Warangal-506332. (T.S)
E-Mail: jitscivil@gmail.com
Mobile: 9490630014

About JITS Narsampet:
Jayamukhi Institute of Technological Sciences, Narsampet is sponsored by Jayamukhi Educational Society, Warangal. The Institute started in a humble way with Three B.Tech. Courses namely ECE, CSE and IT with an intake of 60 each in the year 2001. In a short span of 15 years it has developed as one of the premier Technical Institute in this part of the state. At present the institute offers 5 B.Tech Courses in ECE, CSE, EEE, ME, CIVIL and M.Tech courses in CSE, SWE, VLSI, PSCA and CAD/CAM in addition to MBA, Pharmacy, B.Ed, D.Ed courses under the fold of the same society. The Institute affiliated to JNTU, Hyderabad. Four B.Tech courses namely CSE, ECE, EEE & IT are accredited by NBA AICTE, New Delhi for period of 3 years with effect from 10-02-2009, which is a record in the history of technical institutions of T.S.

The institute is spread over an area of 40 acres of green pastures in Narsampet, about 30km away from the historical city of Warangal and presents a picturesque and panoramic view. It has well developed laboratories in all disciplines, a Computer Centre with 1000 P-IV systems and with 2 IBM servers and associated software, a fully fledged training and Placement Cell, Educational Technology Cell, English Language Communication Skills Laboratory, a dedicated leased BSNL 1:1 8 Mbps internet Wi-Fi Connectivity spread over the entire campus The institute has well qualified and experienced teaching faculty. The faculty is active in research and published more than 100 technical Research papers in reputed National and International journals.

About CIVIL DEPARTMENT
The Department of Civil Engineering was established in the year 2013.The current intake in UG program is 120 The department has dedicated and qualified faculty as per the norms of AICTE. The department has equipped with sophisticated laboratories.

ONE WEEK WORKSHOP ON
"ENHANCEMENT OF SAP SOFTWARE FOR CIVIL ENGINEERS
26th to 31th August-2019

Convenor
Mr.Gulam Samdani
Head, Dept.of Civil Engineering

Coordinator
Miss.Ch.Shalini
Asst. Prof –CED

Organized by

Estd :2001

DEPARTMENT OF CIVIL ENGINEERING
JAYAMUKHI
Institute of Technological Sciences
Narsampet, Warangal-506332 (T.S), INDIA.
Tel (08718) 208586, Fax: (08718) 222569
Website: www.jits.in

ABOUT THE COURSE:
This course titled " E-TABS Complete Course From Beginner to Pro" teaches you ETABS is a highly efficient analysis and design program developed especially for building systems. It is loaded with an integrated system with an ability to handle the largest and most complex building models and configurations.

The lecture focus on the real-time work-related concepts, issues, which is enhanced by the inputs of the instructor's practical experience. It Gives knowledge to Civil Engineers, Entry or senior level, who wishes to streamline their existing knowledge to make a career in Structural planning for various building system.

COURSE CONTENTS:
❖ Complete knowledge of E-TABS
❖ The software has powerful CAD-like drawing tools with a graphical and object-based interface.
❖ It increases the productivity of structural engineers.
❖ The course will help the candidates to acquire in-depth details about the different procedures and simplified analysis aspects of the design models.
❖ Export the designed data to excel for further calculations.
Do preliminary design of structural members.

Eligibility:
The program is for the faculty of AICTE approved Engineering Colleges teaching and doing research in Civil Engineering.

TA, Boarding & Lodging Facilities:
No TA, Boarding and Lodging Facilities will be provided to the out station participants in the college hostels. Local participants will be provided working lunch, tea and snacks along with other participants.

Resource Persons:
Renowned academicians and researchers in this field are being invited to deliver lectures on specialized topics. Some of the eminent guest faculties in this field are:
1. Er.Shravan Kumar Project Manager, MS Tech IT Solutions

Chief Patron:
Sri Ch.Narsimha Reddy - President

Patron:
Sri Ch.Devender Reddy - Secretary
Sri T.V.R.N.Reddy - Joint-Secretary

Principal
Dr.M.Lokanadha Rao

Director
Prof.P.Rangaiah,

Convener:
Mr.Gulam Samdani– Head, CED

Coordinator:
Miss.Ch.Shalini -Asst.Prof

Organizing Committee:
1. Ms.M SriKavya, Asst Prof
2. Mr S.Venkatesh, Asst Prof
3. Mr Syed Saminllah, Asst Prof
4. Mr DS Shiva Kumar, Asst Prof
5. Ms A Sindhuja, Asst Prof
6. Mr YM Punith - Asst Prof
7. Ms.B.H.Jenee, Asst.Prof
8. Ms R Laxmi, Asst Prof
9. Mr.A.Shivlshankar, Asst.Prof
10.Ms.G.Swathi, Asst.Prof
11.Mr.L.Naveen, Asst.Prof

ONE WEEK WORKSHOP ON
"ENHANCEMENT OF SAP SOFTWARE FOR CIVIL ENGINEERS
21th to 26th August-2019

JAYAMUKHI
Institute of Technological Sciences
Narsampet, Warangal-506332 (T.S), INDIA.
Tel (08718) 208586, Fax: (08718) 222569
Website: www.jits.in

1. Full Name: _____
2. Designation: _____
3. Institute: _____
4. Qualification: _____
5. Date of Birth: _____
6. Address for
   Correspondence: _____

   Email: _____
   Mobile: _____
   Fee: 1000/-
Mode of Payment: _____

This is to be certify that the information given by me is correct to the best of my knowledge.

Date: Signature.

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NARSAMPET, WARANGAL - 506332

Estd:2001

Enhancement of SAP Software for Civil Engineer

CONTENTS

- ❖ Introduction to Recent Advances in software and Importance of SAP Software of Construction Techniques

- ❖ Recent Advances in Software of Construction Methods

- ❖ Recent Advances in software of Construction Materials

- ❖ Introduction in software of Construction Management

- ❖ Importance of Construction Engineering in Recent Scenario of Indian Market.

- ❖ Foundation Engineering and its application to Construction Engineering.

Enhancement of SAP Software for Civil Engineer

COURSE OUT COMES

➢ This workshop is students will get the hands-on with SAP Software. It is the only software which provides complete Structural details.

➢ This workshop aims the full range of Structural details, Construction processes, building services and Structural Drawings.

➢ The lecture focus on the real-time work-related concepts, issues, which is enhanced by the inputs of the instructor's practical experience.

➢ It Gives Knowledge to Civil Engineers, Entry or senior level, who wishes to streamline   their existing, knowledge to make a career in Structural planning for various building system.

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Estd:2001

Enhancement of E-TAB Software for Civil Engineer

Sponsoring & Forwarding

This is to certify that Prof./Dr./Sri./Ms. _____ working as _____ in the _____ Department of _____ is sponsored to attend ONE WEEK WORKSHOP ON "ENHANCEMENT OF E-TABS SOFTWARE FOR CIVIL ENGINEERS 2018" Organized by Jayamukhi Institute of Technological Sciences, Narsampet, and Dist. Warangal from 21/10/2019 to 26/10/2019. In case the candidate is selected he/she shall be permitted to attend the course.

Date: _____ Signature of the authorized person.

Name:
Designation.
(SEAL)

Registration fee:
Registration fee of Rs.1000/- has to be paid along with registration form. It has to be paid in the form of crossed demand draft in favor of "Principal, Jayamukhi Institute of Technological Sciences Narsampet" payable at Warangal.

Schedule:
1. The Applications in the prescribed form after payment of fee has to reach the convenor either by e-mail and/or by hard copy on or before 04th October, 2018.
2. Selected candidate will be informed by e-mail on 05th October, 2017.

Address for Correspondence.
The Convenor.
ESSCE 2018
Department of Civil Engineering
Jayamukhi Institute of Technological Sciences,
Narsampet,(Dist): Warangal-506332. (T.S)
E-Mail: jitscivil@gmail.com
Mobile: 9490600014

About JITS Narsampet:
Jayamukhi Institute of Technological Sciences, Narsampet is sponsored by Jayamukhi Educational Society, Warangal. The Institute started in a humble way with Three B.Tech. Courses namely ECE, CSE and IT with an intake of 60 each in the year 2001. In a short span of 15 years it has developed as one of the premier Technical Institute in this part of the state. At present the institute offers 5 B.Tech Courses in ECE, CSE, EEE, ME, CIVIL and M.Tech courses in CSE, SWE, VLSI, PSCA and CAD/CAM in addition to MBA, Pharmacy, B.Ed, D.Ed courses under the fold of the same society. The Institute affiliated to JNTU, Hyderabad. Four B.Tech courses namely CSE, ECE, EEE & IT are accredited by NBA AICTE, New Delhi for period of 3 years with effect from 10-02-2009, which is a record in the history of technical institutions of T.S.

The institute is spread over an area of 40 acres of green pastures in Narsampet, about 30km away from the historical city of Warangal and presents a picturesque and panoramic view. It has well developed laboratories in all disciplines, a Computer Centre with 1000 P IV systems and with 2 IBM servers and associated software, a fully fledged training and Placement Cell, Educational Technology Cell, English Language Communication Skills Laboratory, a dedicated leased BSNL 1:1 8 Mbps internet Wi-Fi Connectivity spread over the entire campus. The institute has well qualified and experienced teaching faculty. The faculty is active in research and published more than 100 technical Research papers in reputed National and International Journals.

About CIVIL DEPARTMENT
The Department of Civil Engineering was established in the year 2013. The current intake in UG program is 120. The department has dedicated and qualified faculty as per the norms of AICTE. The department has equipped with sophisticated laboratories.

ONE WEEK WORKSHOP ON "ENHANCEMENT OF E-TABS SOFTWARE FOR CIVIL ENGINEERS 21th to 26th October 2019

Convenor
Mr.Gulam Samdani
Head, Dept.of Civil Engineering

Coordinator
Miss.Ch.Shalini
Asst. Prof -CED

Organized by

Estd -2001

DEPARTMENT OF CIVIL ENGINEERING
JAYAMUKHI
Institute of Technological Sciences
Narsampet, Warangal 506332 (T.S), INDIA.
Tel (08718) 208586, Fax: (08718) 222569
Website: www.jits.in

ABOUT THE COURSE:
This course titled " E-TABS Complete Course: From Beginner to Pro" teaches you ETABS is a highly efficient analysis and design program developed especially for building systems. It is loaded with an integrated system with an ability to handle the largest and most complex building models and configurations.

The lecture focus on the real-time work-related concepts, issues, which is enhanced by the inputs of the instructor's practical experience. It Gives Knowledge to Civil Engineers, Entry or senior level, who wishes to streamline their existing knowledge to make a career in structural planning for various building system.

COURSE CONTENTS:
❖ Complete knowledge of E TABS
❖ The software has powerful CAD-like drawing tools with a graphical and object-based interface.
❖ It increases the productivity of structural engineers.
❖ The course will help the candidates to acquire in depth details about the different procedures and simplified analysis aspects of the design models.
❖ Export the designed data to excel for further calculations.
Do preliminary design of structural members.

Eligibility:
The program is for the faculty of AICTE approved Engineering Colleges teaching and doing research in Civil Engineering.

TA, Boarding & Lodging Facilities:
No TA, Boarding and Lodging Facilities will be provided to the out station participants in the college hostels. Local participants will be provided working lunch, tea and snacks along with other participants.

Resource Persons:
Renowned academicians and researchers in this field are being invited to deliver lectures on specialized topics. Some of the eminent guest faculties in this field are:
1. Er.Shravan Kumar Project Manager, MS Tech IT Solutions

Chief Patron:
Sri Ch.Narsimha Reddy - President

Patron:
Sri Ch.Devender Reddy - Secretary
Sri T.V.R.N.Reddy - Joint Secretary

Principal
Dr.M.Lokanadha Rao

Director
Prof.P.Rangaiah.

Convener:
Mr.Gulam Samdani– Head, CED

Coordinator:
Miss.Ch.Shalini –Asst.Prof

Organizing Committee:
1. Ms.M.SriKavya, Asst.Prof
2. Mr.S.Venkatesh, Asst.Prof
3. Mr.Syed Samiullah, Asst.Prof
4. Mr.DS Shiva Kumar, Asst.Prof
5. Ms.A.Sindhuja, Asst.Prof
6. Mr.YM Punith - Asst.Prof
7. Ms.D.H Joice, Asst.Prof
8. Ms.B.Laxmi, Asst.Prof.
9. Mr.A.ShivShankar, Asst.Prof
10.Ms.G.Swathi, Asst.Prof
11.Mr.L.Naveen, Asst.Prof

ONE WEEK WORKSHOP ON "ENHANCEMENT OF E-TABS SOFTWARE FOR CIVIL ENGINEERS 21st to 26th October-2019

JAYAMUKHI
Institute of Technological Sciences
Narsampet, Warangal-506332 (T.S), INDIA.
Tel (08718) 208586, Fax: (08718) 222569
Website: www.jits.in

1. Full Name: _____
2. Designation: _____
3. Institute: _____
4. Qualification: _____
5. Date of Birth: _____
6. Address for Correspondence: _____

Email: _____
Mobile: _____
Fee: 1000/-
Mode of Payment: _____

This is to certify that the information given by me is correct to the best of my knowledge.

Date: _____ Signature: _____

JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES

UGC - AUTONOMOUS

Permanently Affiliated to JNTUH, Accredited by NBA, Recognised Under UGC 2 (f) & 12 (b)

NARSAMPET, WARANGAL - 506332

Estd:2001

Enhancement of E-TAB Software for Civil Engineer

CONTENTS

- ❖ Introduction to Recent Advances in software of Construction Techniques

- ❖ Recent Advances in Software of Construction Methods

- ❖ Recent Advances in software of Construction Materials

- ❖ Introduction in software of Construction Management

- ❖ Importance of Construction Engineering in Recent Scenario of Indian Market.

- ❖ Foundation Engineering and its application to Construction Engineering.

Enhancement of E-TAB Software for Civil Engineer

COURSE OUT COMES

- ➢ This workshop is students will get the hands-on with E-Tabs Software. It is the only software which provides complete Structural details.
- ➢ This workshop aims the full range of Structural details, Construction processes, building services and Structural Drawings.
- ➢ The lecture focus on the real-time work-related concepts, issues, which is enhanced by the inputs of the instructor's practical experience.
- ➢ It Gives Knowledge to Civil Engineers, Entry or senior level, who wishes to streamline their existing, knowledge to make a career in Structural planning for various building system.

ROBOTICS

Eligibility:
The program is open to the all students in Electronics and Communication Engineering courses at under graduate and post graduate level are welcome.

TA, Boarding & Lodging Facilities:
All the participants will be provided working lunch, tea and snacks. No TA, Boarding and Lodging Facilities will be provided.

Registration Details:
- For UG/PG Students   100/-
- Research Scholars   200/-

How to Register:
The participants may log on to the website www.jits.in and fill up the application form selecting the name of the course you may also send scanned copy of your filled application form to eeehod@jits.in Application format may be downloaded from the website (also available in this brochure) you can pay the fee through Google/ phone pay: 9989418318(verification details: kalyan)

Address for Correspondence:
The Convenor,
Head of the Department
Dept of Electronics and Communication Engineering,
Jayamukhi Institute of Technological Sciences,
Narsampet, (Dist), Warangal-506332, (T.S)
E-mail: eeehod@jits.in
Mobile: 9989327766.

Chief Patron:
Sri Ch.Narsimha Reddy - President

Patron:
Sri Ch.Devender Reddy - Secretary
Sri T.V.R.N.Reddy - Joint-Secretary

Principal
Dr.M.Lokanadha Rao

Director
Prof.P.Rangaiah,

Convenor:
Mr.MD.HAMEED PASHA
Associate Professor & Head, Dept of .ECE

Coordinators:
Mr. S.KALYAN
Asst.Prof. Dept of .ECE
Cellno: 9989118318,.

Mr. N.Sukumar
Asst.Prof. Dept of .ECE
Cellno: 9885577504.

Schedule:
1. Last date of receipt of applications: Feb 08th, 2020
2. Spot registrations allowed

The Applications in the prescribed form has to be reach coordinator either Email and/ or by hard copy.

programme sponsored by AICTE and many workshops, seminars and guest lectures for the benefits of Students and Faculty Members

ONE-WEEK WORKSHOP ON
"ROBOTICS"
10th To 15th FEB 2020

ROBOTICS

Organized by:

**DEPARTMENT OF
ELECTRONICS AND COMMUNICATION
ENGINEERING**

**JAYAMUKHI
INSTITUTE OF TECHNOLOGICAL SCIENCES**
(U.G.C AUTONOUMOUS)
Permanently Affiliated to JNTUH, Accredited by NBA,
Narsampet, Warangal – 506332.
Tel (08718) 208586, Fax: (08718) 222869
Website: www.jits.in

JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES

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NARSAMPET, WARANGAL - 506332

Estd:2001

ROBOTICS

List of Content:

1. Detail study on signal processing technique
2. Advance study on modulation technique.
3. Voice/speech Recognition
4. DC-motor
5. Arduino Programming
6. Design and fabrication of voice controlled robot
7. Bluetooth/ESP8266 Wi-Fi module technology

ROBOTICS

Course outcomes:

1. Understand the concepts of signal processing and modulation techniques.

2. Detail study on Voice/speech recognition and DC-Motor

3. Ability to understand the Arduino Programming concepts

4. Design and fabrication of various robots which is used in real world applications

DESIGN & FABRICATION OF PCB

Eligibility:

The program is open to the all students of Electronics & Communication Engineering courses at undergraduate and post graduate level re welcome.

Registration Details:

- Students   : Free
- Research Scholars   Free

How to Register:

The participants may log on to the website www.jits.in and fill up the application form selecting the name of the course you may also send scanned copy of your filled application form. Application format may be downloaded from the website(also available in this Brochure).

Address for Correspondence:
The Convener,
Head of the Department
Dept of ECE
Jayamukhi Institute of Technological Sciences, Narsampet, (Dist): Warangal-506332. (T.S)
Email: hameedjits@gmail.com
Mobile: 9490377766.
Mobile: 7559839692.

Chief Patron:

Sri Ch.Narsimha Reddy - President

Patron:

Sri Ch.Devender Reddy - Secretary
Sri T.V.R.N.Reddy - Joint-Secretary

Principal -
Dr. M.Lokirandha Rao

Director
Prof.P.Rangaiah,

Convener:
Mr.Md.Hameed Pasha
Associate Professor &Head, ECE Department
Coordinator:
Mrs.S.Sandhya Rani
Assoc.Prof.ECE Department
Cell: 9030079969, 9849829990,
Ms.S.Priyanka
Asst.Prof.ECE Department
Cell: 9951659502

Schedule:

1. Last date of receipt of applications:
18ᵗʰ January, 2020

2. Spot registrations allowed

The Applications in the prescribed form has to be reach coordinator either Email and/ or by hard copy.

One week WORKSHOP ON
"DESIGN & FABRICATION OF PCB"
20ᵗʰ -24ᵗʰ January,2020

Organized by:

DEPARTMENT OF
Electronics and Communication Engineering

JAYAMUKHI

INSTITUTE OF TECHNOLOGICAL SCIENCES
(U.G.C AUTONOUMOUS)
Permanently Affiliated to JNTUH, Accredited by NBA
Narsampet, Warangal - 506332.
Tel:(08718) 708358, Fax: (08718) 222569
Website: www.jits.in

About JITS Narsampet

Jayamukhi Institute of Technological Sciences, Narsampet Sponsored by the Jayamukhi Educational Society the Jayamukhi Institute of Technological Sciences came into being in 2001 to provide quality and contemporary education with social relevance in the engineering faculty with an ultimate vision to maintain global standards in higher learning and research. The Institute has the approval of AICTE and recognized by the Government of Telangana. It is permanently affiliated to Jawaharlal Nehru Technological University (JNTU), Hyderabad, UGC Autonomous & accredited by NBA.

The Institute has come upon 40 acres of green pasture in Narsampet, about 30km away from the historic city of Warangal and presents a picturesque and panoramic view. JITS offers a four-year B.Tech. programme in the disciplines of CSE, ECE, EEE,CIVIL and ME.

Department of ECE

Department of Electronics and Communication Engineering has set vision "To be a premier department for education in Electronics and Communication Engineering in state of Telangana, moulding students into professional engineers". The department imparts state of the art education and encourages cutting edge research. It offers UG program, PG programs in VLSI and ES. Well equipped Laboratories have been facilitated for the Students, research Scholars and the 50 Lakhs of funds generated from AICTE under various schemes like MODROBS, FDPs, STTPs. Received TEQIP-III fund from JNTU Hyderabad.

Established two laboratories namely Internet of Things(IoT) and Robotics sponsored by Texas Instruments USA.

About the Workshop:

The electronics and manufacturing industry with growth demands for skill set of the individual. With the competitive market, industrial needs and decreasing demand for work force, it is always advised to learn new things and have certain skill set under sleeves to work in industry. This course is designed to train students in Printed Circuit Board design, Drafting and Fabrication.

PCB manufacturing is the construction of your board design. This is a two-step process that begins with board fabrication and ends with printed circuit board assembly (PCBA).

List The following list of resource Person:

1. Prof.K.Kishan Rao, Professor, SNIST, Hyderabad
2. Dr.Ghanshyam singh, Professor, of ECE, JITS
3. Dr.P Krishna, Professor, of ECE, JITS
4. Dr.M.krishna Samy, Professor of ECE,JITS
5. Prof.M.Sidhanth Babu, Professor of EC E, JITS
6. Prof.A.Janardhan, Professor of ECE, JITS

One week WORKSHOP ON
"DESIGN & FABRICATION OF PCB"
20ᵗʰ -24ᵗʰ January,2020

REGISTRATION FORM

1. Name of Participant:
2. Roll No:
3. Institution:
4. Address for Correspondence:

5. Mobile No:
6. Email:
7. Category:

DECLARATION

The information provided herewith is true to the best of my knowledge. I agree to abide by the rules and regulations governing the workshop programme.

Date:

Place:
                    Signature of the applicant

Signature of the Sponsoring Authorities

JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES

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NARSAMPET, WARANGAL - 506332

Estd:2001

DESIGN & FABRICATION OF PCB

CONTENTS:

1.Introduction to Computer Aided Design of electronics

2.Fundamental of basic electronics

- Component identification

- Component symbols & their footprints

3.Schematic Design using OrCAD

4.Layout Design using CADSTAR

5.Design rule checking

6.Manufacturing exports

7.Fabrication of PCB

8.Assembling & Testing

DESIGN & FABRICATION OF PCB

COURSE OUTCOMES

Upon the completion of this course, students will able to:

CO 1 : Understand the various symbols, foot prints/packaging of components

CO 2: Design schematic and layout of electronic circuits

CO 3: Fabricate Printed Circuit Board

CO 4: Evaluate and test the completed PCB

ROBOTICS AND ITS APPLICATIONS

List of Content:

8. Detail study on signal processing technique

9. Advance study on modulation technique.

10. Voice/speech Recognition

11. DC-motor

12. Arduino Programming

13. Design and fabrication of voice controlled robot

14. Bluetooth/ESP8266 Wi-Fi module technology

ROBOTICS AND ITS APPLICATIONS

Course outcomes:

1. Understand the concepts of signal processing and modulation techniques.

5. Detail study on Voice/speech recognition and DC-Motor

6. Ability to understand the Arduino Programming concepts

7. Design and fabrication of various robots which is used in real world applications

BRAIN WAVE ROBOTICS & ITS APPLICATIONS

CONTENTS:

1. Introduction to Embedded Systems
2. Application of Embedded Systems in Robotics
3. Introduction to Robotics,,
4. Operator Interface,
5. Introduction to Arduino,
6. Artificial Intelligence
7. Introdcution to IoT,
8. Robotics Applications
9. Machine Learning in Robotics,
10. Programming

BRAIN WAVE ROBOTICS & ITS APPLICATIONS

COUSRE OUTCOMES

1. Able to apply robot fundamentals in designing various types of end effecters
2. Able to design end effectors required for Artificial Intelligence applications
3. Able to determine the robot trajectory to robotic motion by applying Robotic basics
4. Able to select Sensors for applications of Artificial Intelligence
5. Able to work on Real Time Hardware modules based on Industrial applications

Days 5 Workshop on Composite Materials

Days 5 Workshop on Composite Materials

JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES

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NARSAMPET, WARANGAL - 506332

Estd:2001

R.F. DESIGN USING LATEST CMOS TECHNIQUES

Eligibility:
The program is open to the all student members Electronics and Communication Engineering courses at undergraduate and post graduate level are welcome.

Registration Details:
IEEE /ISTE Students Free   Non IEEE
/ISTE Students   100/-

How to Register:
The participants may log on to the website: www.jits.in and fill up the application form selecting the name of the course you may also send scanned copy of your filled application form to gshyamsingh2@gmail.com or ecehod@jits.in. Application format may be downloaded from the website (also available in this brochure). For registration you can pay the fee through Google Pay/Phone Pe, to this no. 9490377765.

Address for Correspondence:
Head of the Department
Md. Hameed Pasha
Dept. of Electronics and Communication Engineering
Jayamukhi Institute of Technological Sciences,
Narsampet, (Dist) Warangal-506332. (T.S)
Email: hodecejits@gmail

About JITS Narsampet:
Jayamukhi Institute of Technological Sciences, Narsampet is sponsored by Jayamukhi Educational Society, Warangal. The Institute started in a humble way with 3 B.Tech. Courses namely ECE, CSE and IT with an intake of 60 each in the year 2001. In a short span of 19 years it has developed as one of the premier Technical Institute in this part of the state. At present the institute offers 5 B.Tech Courses in ECE, CSE, EEE, ME, CIVIL and M.Tech courses in CSE, SWE, VLSI, PSCA and CAD/CAM in addition to MBA, Pharmacy, B.Ed, D.Ed courses under the fold of the same society. The Institute affiliated to JNTU, Hyderabad. Four B.Tech courses namely CSE, ECE, EEE &IT are accredited by NBA AICTE, New Delhi for period of three years with effect from 10-02-2009, which is a record in the history of technical institutions of T.S. Again three departments EEE, CSE, ECE was accredited by NBA for a period of three years. The institute is spread over an area of 40

Chief Patron:
Sri Ch. Narsimha Reddy - President
Patron:
Sri Ch. Devender Reddy - Secretary
Sri T.V.R.N. Reddy - Joint-Secretary
Principal
Dr. M. Lokanadha Rao
Director
Prof. P. Rangaiah.
Head of Department
Md. Hameed Pasha
Convener:
Dr.Ghanshyam Singh
Professor, Electronics and Communication Engineering
ISTE Circuit & System Faculty Advisor

Dr. S.K. Mastan
Associate Professor
ECE Dept.
Cell no: 9949676900, 6281815605
Coordinators:
Ms. Zulekha Tabassum
Asst. Prof., Electronics and Communication Engineering
IEEE /ISTE Faculty Advisor
Mr. M.Sridhar, Asst. Prof, ECE Dept.
M, Sukumar, Asst. Prof, ECE Dept.
Mr. Kalyan, Asst. Prof, ECE Dept.
Mr. Rakesh, Asst. Prof, ECE Dept.
Mr. Lalu, Asst. Prof, ECE Dept.

Schedule:
Last date of receipt of applications:
November 10th, 2019
Spot registrations allowed

The Applications in the prescribed form has to be reach coordinator either Email and/ or by hard copy.
Programme sponsored by AICTE and many workshops, seminars and guest lectures for the benefits of Students. **About the Workshop:**
The State-of-the-art design approaches for low-voltage radio frequency (RF) and millimeter-wave (mm-wave)

HANDS-ON WORKSHOP ON
" R.F. DESIGN USING LATEST CMOS TECHNIQUES "
11th – 16th November 2019

Organized by:

DEPARTMENT OF
Electronics and Communication Engineering
&
IETE STUDENT CHAPTER

JAYAMUKHI
INSTITUTE OF TECHNOLOGICAL SCIENCES
(U.G.C AUTONOUMOUS)
Permanently Affiliated to JNTUH, Accredited by NBA,
Narsampet, Warangal – 506332.
Tel (08718) 208586, Fax: (08718) 222569
Website: www.jits.in

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acres of green pastures in Narsampet, about 30km away from the historical city of Warangal and presents a picturesque and panoramic view. It has well developed laboratories in all disciplines, a Computer Centre with 1080 P-IV systems and with 2 IBM servers and associated software, a fully fledged training and Placement Cell, Educational Technology Cell, English Language Communication Skills Laboratory, a dedicated leased BSNL 13 BMbps Internet Wi-Fi Connectivity spread over the entire campus. The institute has well qualified and experienced teaching faculty. The faculty is active in research and published more than 200 technical Research papers in reputed National and International Journals.

About the Department of Electronics and Communication Engineering
The Electronics and Communication Engineering (ECE) Department was started in the year 2001 and it is one of the well established and largest departments in the institution since the inception. The department offers Undergraduate (B. Tech ECE) and Postgraduate (M. Tech Embedded Systems) & (M. Tech VLSI Design) Programs that provide students with the knowledge and tools they need to succeed in the Electronics and Communication Engineering. Currently the department offers four year B.Tech course with student intake of 180. The department is accorded with NBA accreditation to the year 2009 and 2017 for 3 Years. The Department has grant in aids from various funding agencies for organizing FDPs, STTPs, MODROBs Schemes over 3 Years. The senior faculties of the department are executing TEQIP-III project under JNTU Hyderabad and also applied for DST projects.
Texas Instruments University program sponsored two research Laboratories on the Internet of Things and Robotics. Students of the department are well placed in MNCs and other government sectors.

CMOS circuits. Effective design techniques at RF/mm-wave frequencies are described, including body biasing in fully depleted (FD) silicon-on-insulator (SOI) CMOS technologies and circuit topologies based on integrated reactive components (i.e., capacitors, inductors and transformers). The application of low-voltage design techniques is discussed for the main RF/mm-wave circuit blocks, i.e., low-noise amplifiers (LNAs), mixers and power amplifiers (PAs), highlighting the main design trade-offs A crucial role in RF/mm-wave, low-voltage operation is performed by the reactive components, especially inductive components (i.e., integrated Inductors and transformers) that can be properly exploited to compensate for the limited voltage headroom. Another important contribution is also given by the body biasing approach, especially in fully depleted (FD) silicon-on-insulator (SOI) platforms [2], which further improves IC performance at low-voltage supply levels.

Resource Persons
Renowned academicians in this field are being invited to deliver lectures on specialized topics. Some of the eminent guest faculties in this field are:
List of Resource Persons:

List of Resource Persons:
1. Mr. Anith Mishra,
   Embedded developer,
   Incavians Technologies.

2. Dr.Ghanshyam Singh
   Professor, ECE

3. Dr. M.Krishnasamy

HANDS-ON WORKSHOP ON
" R.F. DESIGN USING LATEST CMOS TECHNIQUES "
11th – 16th November 2019

REGISTRATION FORM

1. Name of Participant:...........................

2. Department: ....

3. Institution:...........................

4. Address for
   Correspondence:...........................
   ....

5. Mobile No:...........................

6. Email:...........................

7. Category
   (SC/S T/Others):...........................

DECLARATION
The information provided herewith is true to the best of my knowledge. I agree to abide by the rules and regulations governing the workshop programme.

Date:
Place:

Signature of the applicant

Signature of the Sponsoring Authorities

R.F. DESIGN USING LATEST CMOS TECHNIQUES

COURSE CONTENT

1. Introduction to low-voltage radio frequency (RF) and millimeter-wave (mm-wave).
2. CMOS circuits. Effective design techniques at RF/mm-wave frequencies.
3. Including body biasing in fully depleted (FD) silicon-on-insulator (SOI) CMOS technologies and circuit topologies based on integrated reactive components.
4. Application of low-voltage design techniques is discussed for the main RF/mm-wave circuit blocks, i.e., low-noise amplifiers.
5. Important contribution is also given by the body biasing approach, especially in fully depleted (FD) silicon-on-insulator (SOI) platforms
6. Main design tradeoffs. A crucial role in RF/mm-wave, low-voltage operation

R.F. DESIGN USING LATEST CMOS TECHNIQUES

COURSE OUTCOMES

1. Understanding electromagnetic waves is important to Develop Skills for an RF WPH system.
2. This workshop provides guidance in the RF Design field of various parameters that need to be evaluated, which decides the performance of a WPH Design.
3. This workshop also helped clear concepts of low-power consumption electrical systems; power leakage during transmission may lead to energy insufficiency.
4. Students got the opportunity to learn choosing the appropriate operation frequency to the application's requirement critically dictates the efficiency of the WPH system.
5. The operation ranges also need to be specified. Long range (far-field) power harvesting or transmission requires high frequency such as 2.45, 5.24 GHz.
6. Students learnt for safety issues, RF emission power and frequency must be considered carefully to avoid unwanted damages.

Java Fundamental and Data Base Management Training

<u>**Course Content:**</u>

- ➢ What is Computer, Basic Applications of Computer; Concepts of Hardware and Software, Number System, Number System Conversion.
- ➢ Introduction to the C Language – Algorithm, Pseudo code, Flow chart, Background, C Programs, Java Fundamental with Programming ,Identifiers, Data Types,
- ➢ Variables, Constants, Input / Output, Operators (Arithmetic, relational, logical, bitwise etc
- ➢ Statements- Selection Statements (making decisions) – if and switch statements,
- ➢ Repetition statements (loops)-while, for, do-while statements, Loop examples, other statements related to looping – break, continue, go to, Simple Java Program examples.

Java Fundamental and Data Base Management Training

<u>**Course Outcomes:**</u>

1. Understand computer basics.
2. Understand   C and Java programming basics .
3. Understand binary number system.
4. Use different data types in a computer program.
5. Design programs involving decision structures, loops, arrays and functions.