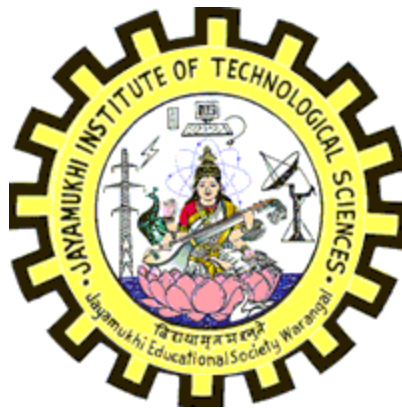


**ACADEMIC REGULATIONS
COURSE STRUCTURE
AND
DETAILED SYLLABUS**

**M.TECH
POWER SYSTEM CONTROL AND
AUTOATION**

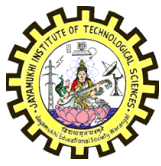
(Applicable for the batches admitted from 2015-2016)



JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES

(UGC-Autonomous)

Narasampet, Warangal – 506 332
Telangana State, India



JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES

(AUTONOMOUS)

Narsampet, Warangal- 506 332. (T.S.)

R 15 - ACADEMIC REGULATIONS (CBCS) FOR M. Tech. (REGULAR) DEGREE PROGRAMMES

Applicable for the students of M. Tech. (Regular) programme from the Academic Year **2015-16** and on wards.

The M. Tech. Degree of JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES shall be conferred on candidates who are admitted to the programme and who fulfill all the requirements for the award of the Degree.

1. ELIGIBILITY FOR ADMISSIONS

Admission to the above programme shall be made subject to eligibility, qualification and specialization as prescribed by the University from time to time.

Admissions shall be made on the basis of merit/rank obtained by the candidates at the qualifying Entrance Test conducted by the University or on the basis of any other order of merit as approved by the University, subject to reservations as laid down by the Govt. from time to time.

AWARD OF M. Tech. DEGREE

A student shall be declared eligible for the award of the M. Tech. Degree, if he pursues a course of study in not less than two and not more than four academic years. However, he is permitted to write the examinations for two more years after four academic years of course work, failing which he shall forfeit his seat in M. Tech. programme.

The student shall register for all 88 credits and secure all the 88 credits.

The minimum instruction days in each semester are 90.

3.0 COURSES OF STUDY

The following specializations are offered at present for the M. Tech. programme of study.

1. CAD/CAM
2. Embedded Systems
3. VLSI Design
4. Power System Control and Automation
5. Computer Science Engineering.
6. Software Engineering.

1. COURSE REGISTRATION

A 'Faculty Advisor or Counselor' shall be assigned to each student, who will advise him on the Post Graduate Programme (PGP), its Course Structure and Curriculum, Choice/Option for Subjects/ Courses, based on his competence, progress, pre-requisites and interest.

Academic Section of the College invites 'Registration Forms' from students with in 15 days from the commencement of class work through 'ON-LINE SUBMISSIONS', ensuring 'DATE and TIME Stamping'. The ON-LINE Registration Requests for any 'CURRENT SEMESTER' shall be completed BEFORE the commencement of SEEs (Semester End Examinations) of the 'PRECEDING SEMESTER'.

A Student can apply for ON-LINE Registration, ONLY AFTER obtaining the 'WRITTEN APPROVAL' from his Faculty Advisor, which should be submitted to the College Academic Section through the Head of Department (a copy of it being retained with Head of Department, Faculty Advisor and the Student).

If the Student submits ambiguous choices or multiple options or erroneous entries - during ON-LINE Registration for the Subject(s) / Course(s) under a given/ specified Course Group/ Category as listed in the Course Structure, only the first mentioned Subject/ Course in that Category will be taken into consideration.

Subject/ Course Options exercised through ON-LINE Registration are final and CANNOT be changed, nor can they be inter-changed; further, alternate choices will also not be considered. However, if the Subject/ Course that has already been listed for Registration (by the Head of Department) in a Semester could not be offered due to any unforeseen or unexpected reasons, then the Student shall be allowed to have alternate choice - either for a new Subject (subject to offering of such a Subject), or for another existing Subject (subject to availability of seats), which may be considered. Such alternate arrangements will be made by the Head of Department, with due notification and time-framed schedule, within the FIRST WEEK from the commencement of Class-work for that Semester.

5 ATTENDANCE

The programmes are offered on a unit basis with each subject being considered a unit.

- 5.1 Attendance in all classes (Lectures/Laboratories etc.) is compulsory. The minimum required attendance in each theory / Laboratory etc. is 75% including the days of attendance in sports, games, NCC and NSS activities for appearing for the End Semester examination. A student shall not be permitted to appear for the Semester End Examinations (SEE) if his attendance is less than 75%.
- 5.2 Condonation of shortage of attendance in each subject up to 10% (65% and above and below 75%) in each semester shall be granted by the College Academic Committee.
- 5.3 Shortage of Attendance below 65% in each subject shall not be condoned.
- 5.4 Students whose shortage of attendance is not condoned in any subject are not

eligible to write their end semester examination of that subject and their registration shall stand cancelled.

- 5.5 A prescribed fee shall be payable towards condonation of shortage of attendance.
- 5.6 A Candidate shall put in a minimum required attendance at least three (3) theory subjects in I Year I semester for promoting to I Year II Semester. In order to qualify for the award of the M.Tech. Degree, the candidate shall complete all the academic requirements of the subjects, as per the course structure.
- 5.7 A student shall not be promoted to the next semester unless he satisfies the attendance requirement of the present Semester, as applicable. They may seek readmission into that semester when offered next. If any candidate fulfills the attendance requirement in the present semester, he shall not be eligible for readmission into the same class.

6 EVALUATION

The performance of the candidate in each semester shall be evaluated subject-wise, with a maximum of 100 marks for theory and 100 marks for practicals, on the basis of Internal Evaluation and End Semester Examination.

2. For the theory subjects 60 marks shall be awarded for the performance in the Semester End Examination and 40 marks shall be awarded for the performance in Internal Examinations. The Internal Evaluation shall be made based on the best of the marks secured in the two Mid Term-Examinations conducted, one in the middle of the Semester and the other, immediately after the completion of Semester instructions. Each mid-term examination shall be conducted for a total duration of 120 minutes with 6 Questions out of which 04 should be answered and each question carries 10 Marks. The End Examination Question paper (Theory) contains 05 questions with internal choice, each question carries 12 Marks.
3. For practical subjects, 60 marks shall be awarded for performance in the Semester End Examinations and 40 marks shall be awarded for day-to-day performance as Internal Marks.
4. For conducting laboratory end examinations of all PG Programmes, one internal examiner and one external examiner are to be appointed by the Principal of the College and the same to be informed to the Controller of Examinations in two weeks before for commencement of the lab end examinations.
5. There shall be two seminar presentations during I year I semester and II semester. For seminar, a student under the supervision of a faculty member, shall collect the literature on a topic and critically review the literature and submit it to the department in a report form and shall make an oral presentation before the Departmental Academic Committee consisting of Head of the Department, Supervisor and two other senior faculty members of the department. For each Seminar there will be only internal evaluation of 50 marks. A candidate has to secure a minimum of 50% of marks to be declared successful. If he fails to fulfill minimum marks, he has to reappear during the supplementary examinations.

6. There shall be a Comprehensive Viva-Voce in II year I Semester. The Comprehensive Viva-Voce is intended to assess the students' understanding of various subjects he has studied during the M. Tech. course of study. The Head of the Department shall be associated with the conduct of the Comprehensive Viva-Voce through a Committee. The Committee consisting of Head of the Department, one senior faculty member. There are no internal marks for the Comprehensive Viva-Voce and evaluates for maximum of 100 marks. A candidate has to secure a minimum of 50% of marks to be declared successful. If he fails to fulfill minimum marks, he has to reappear during the supplementary examinations.
7. A candidate shall be deemed to have secured the minimum academic requirement in a subject if he secures a minimum of 40% of marks in the Semester End Examination and a minimum aggregate of 50% of the total marks in the Semester End Examination and Continuous Internal Evaluation taken together.
8. In case the candidate does not secure the minimum academic requirement in any subject (as specified in 6.6) he has to reappear for the Semester End Examination in that subject.
- 6.8. A candidate shall be given one chance to re-register for the subjects if the internal marks secured by a candidate is less than 50% and failed in that subject for maximum of two subjects and should register within four weeks of commencement of the class work. In such a case, the candidate must re-register for the subjects and secure the required minimum attendance. The candidate's attendance in the re-registered subject(s) shall be calculated separately to decide upon his eligibility for writing the Semester End Examination in those subjects. In the event of the student taking another chance, his Continuous Internal Evaluation (internal) marks and Semester End Examination marks obtained in the previous attempt stands cancelled.
- 6.9. In case the candidate secures less than the required attendance in any subject, he shall not be permitted to write the Semester End Examination in that subject. He shall re-register for the subject when next offered.

7. EVALUATION OF PROJECT/DISSERTATION WORK

- 7.1. Every candidate shall be required to submit a thesis or dissertation on a topic approved by the Project Review Committee.
- 7.2. A Project Review Committee (PRC) shall be constituted with Head of the Department as Chairperson, Project Supervisor and one senior faculty member of the Departments offering the M. Tech. programme.
- 7.3. Registration of Project Work: A candidate is permitted to register for the project work after satisfying the attendance requirement of all the subjects, both theory and practical.
- 7.4. After satisfying 7.2, a candidate has to submit, in consultation with his Project Supervisor, the title, objective and plan of action of his project work to the PRC for approval. Only after obtaining the approval of the PRC the student can initiate the Project work.

- 7.5 If a candidate wishes to change his supervisor or topic of the project, he can do so with the approval of the PRC. However, the PRC shall examine whether or not the change of topic/supervisor leads to a major change of his initial plans of project proposal. If yes, his date of registration for the project work starts from the date of change of Supervisor or topic as the case may be.
- 7.6 A candidate shall submit his project status report in two stages at least with a gap of 3 months between them.
- 7.7 The work on the project shall be initiated at the beginning of the II year and the duration of the project is two semesters. A candidate is permitted to submit Project Thesis only after successful completion of all theory and practical courses with the approval of PRC not earlier than 40 weeks from the date of registration of the project work. For the approval of PRC the candidate shall submit the draft copy of thesis to the Head of the Department and make an oral presentation before the PRC. The student is allowed to apply for Project Viva-Voce only after clearance from PRC.
- 7.8 Three copies of the Thesis certified by the supervisor shall be submitted to the college/school/Institute.
- 7.9 For Project Work Review I in II Year I Sem. there is an internal marks of 50, the evaluation should be done by the PRC for 25 marks and Supervisor will evaluate for 25 marks. The Supervisor and PRC will examine the Problem Definition, Objectives, Scope of Work, Literature Survey in the same domain.
- 7.10 For Project Work Review II in II Year II Sem. there is an internal marks of 50, the evaluation should be done by the PRC for 25 marks and Supervisor will evaluate for 25 marks. The PRC will examine the overall progress of the Project Work and decide the Project is eligible for final submission or not. The final internal Project Work Review marks will be considered as the average marks secured under both Project Work Review meetings (I & II). A candidate has to secure a minimum of 50% of marks to be declared successful for Project Work Review. If he fails to fulfill minimum marks, he has to reappear during the supplementary examination.
- 7.11. For Project Evaluation (Viva Voce) in II Year II Sem. there is an external marks of 150 and the same evaluated by the External examiner appointed Controller of Examinations & Chairman-BOS of the concerned department. The candidate has to secure minimum of 50% marks in Project Evaluation (Viva-Voce) examination.
- 7.12 If he fails to fulfill as specified in 8.11, he will reappear for the Viva-Voce examination only after three months. In the reappeared examination also, fails to fulfill, he will not be eligible for the award of the degree.
- 7.13 The thesis shall be adjudicated by one examiner selected controller of Examinations & Chairman-BOS of the department. For this, the Principal of the College shall submit a panel of 3 examiners, eminent in that field, with the help of the guide concerned and Head of the Department.
- 7.14 If the report of the examiner is not favourable, the candidate shall revise and resubmit the Thesis. If the report of the examiner is unfavourable again, the thesis shall be summarily rejected.

7.15 If the report of the examiner is favourable, Project Viva-Voce examination shall be conducted by a board consisting of the Supervisor, Head of the Department and the external examiner who adjudicated the Thesis.

7.16. The Head of the Department shall coordinate and make arrangements for the conduct of Project Viva- Voce examination.

8 EXAMINATIONS AND ASSESSMENT - THE GRADING SYSTEM

8.1 Marks will be awarded to indicate the performance of each student in each Theory Subject, or Lab/Practicals, or Seminar, or Project, etc., based on the % marks obtained in CIE + SEE (Continuous Internal Evaluation + Semester End Examination, both taken together) as specified in Item 6 above, and a corresponding Letter Grade shall be given.

8.2 As a measure of the student's performance, a 10-point Absolute Grading System using the following Letter Grades (UGC Guidelines) and corresponding percentage of marks shall be followed:

% of Marks obtained in a Course	Letter Grade	Grade Point
>=80 to 100	O (Outstanding)	10
>=70 to < 80	A+ (Excellent)	9
>=60 to < 70	A (Very Good)	8
>=55 to < 60	B+(Good)	7
>=50 to < 55	B (Above Average)	6
>=45 to < 50	C (Average)	5
>=40 to < 45	P (Pass)	4
Less than 40	F (Fail)	0
0	Ab (Absent)	0

8.3 A student obtaining F Grade in any Subject shall be considered 'failed' and is be required to reappear as 'Supplementary Candidate' in the Semester End Examination (SEE), as and when offered. In such cases, his Internal Marks (CIE Marks) in those Subjects will remain the same as those he obtained earlier.

8.4 A student not appeared for examination then 'Ab' Grade will be allocated in any Subject shall be considered 'failed' and will be required to reappear as 'Supplementary

Candidate' in the Semester End Examination (SEE), as and when offered.

- 8.5 A Letter Grade does not imply any specific Marks percentage and it will be the range of marks percentage.
- 8.6 In general, a student shall not be permitted to repeat any Subject/ Course (s) only for the sake of 'Grade Improvement' or 'SGPA/ CGPA Improvement'.
- 8.7 A student earns Grade Point (GP) in each Subject/ Course, on the basis of the Letter Grade obtained by him in that Subject/ Course. The corresponding 'Credit Points' (CP) are computed by multiplying the Grade Point with Credits for that particular Subject/ Course.

Credit Points (CP) = Grade Point (GP) x Credits For a Course

- 8.8 The Student passes the Subject/ Course only when he gets **GP ≥ 6(B Grade or above)**.
- 8.9. The Semester Grade point average (SGPA) is calculated by dividing the sum credit points (Σ CP). Secured from all subjects/courses registered in a semester, by the total number of credits registered during that semester. SGPA is rounded off to Two Decimal Places. SGPA is thus computed as

$$\left\{ \sum_{i=1}^N c_i G_i \right\} / \left\{ \sum_{i=1}^N c_i \right\} \quad \dots \text{For each semester}$$

- 8.10 The Cumulative Grade Point Average (CGPA) is a measure of the overall cumulative performance of a student over all Semesters considered for registration. The CGPA is the ratio of the Total Credit Points secured by a student in all registered Courses (with an exemption of 8 credits in electives subjects) in all semesters. CGPA is rounded off to two decimal places. CGPA, is thus computed from the I year, Second-Semester onwards, at the end of each semester, as per the formula.

$$\left\{ \sum_{j=1}^M c_j G_j \right\} / \left\{ \sum_{j=1}^M c_j \right\} \quad \dots \text{for all 'S' semesters registered}$$

(i.e., upto and inclusive of 'S' semester, $S \geq 2$)

Where "M" is the total no. of Subjects (as specifically required and listed under the Course Structure of the parent Department) the student has 'REGISTERED' from the 1st Semester onwards upto and inclusive of the semester S (obviously $M > N$), 'j' is the subject indicator index takes into account all subjects from 1 Subject and G_i represents the Grade Points (GP) corresponding to the Letter Grade awarded for that jth subject. After registration and completion of I year I semester however, the SGPA of that Semester itself may be taken as the CGPA, as there are no cumulative effects.

- 8.11 For Calculations listed in Item 8.6 – 8.10, performance in failed Subjects/ Courses (securing F Grade) will also be taken into account, and the Credits of such Subjects/ Courses will also be included in the multiplications and summations.

9. WITHHOLDING OF RESULTS

If the student has not paid fees to College at any stage or has pending dues against his name due to any reason whatsoever, or if any case of indiscipline is pending against him, the result of the student may be withheld, and he will not be allowed to go into the next higher semester. The Award or issue of the Degree may also be withheld in such cases.

10. AWARD OF DEGREE AND CLASS

10.1 A Student who registers for all the specified Subjects/ Courses as listed in the Course Structure, satisfies all the Course Requirements, and passes the examinations prescribed in the entire PG Programme (PGP), and secures the required number of **88** Credits (with $CGPA \geq 6.0$), shall be declared to have 'QUALIFIED' for the award of the M.Tech. Degree in the chosen Branch of Engineering and Technology with specialization as he admitted.

10.2 AWARD OF CLASS

After a student has satisfied the requirements prescribed for the completion of the programme and is eligible for the award of M. Tech. Degree, he shall be placed in one of the following three classes based on the CGPA:

Class Awarded	CGPA
First Class with Distinction	≥ 7.75
First Class	$6.75 \leq CGPA < 7.75$
Second Class	$6.0 \leq CGPA < 6.75$

10.3 A student with final CGPA (at the end of the PGP) < 6.00 will not be eligible for the Award of Degree.

11. GENERAL

- Credit:** A unit by which the course work is measured. It determines the number of hours of instructions required per week. One credit is equivalent to one hour of teaching (lecture or tutorial) or two hours of practical work/field work per week.
- Credit Point:** It is the product of grade point and number of credits for a course.
- Wherever the words "he", "him", "his", occur in the regulations, they include "she", "her".
- The academic regulation should be read as a whole for the purpose of any interpretation.

- e. In the case of any doubt or ambiguity in the interpretation of the above rules, the decision of the Principal is final.
- f. The College may change or amend the academic regulations or syllabi at any time and the changes or amendments made shall be applicable to all the students with effect from the dates notified by the College.

MALPRACTICES RULES

DISCIPLINARY ACTION FOR / IMPROPER CONDUCT IN EXAMINATIONS

	Nature of Malpractices/Improper Conduct	Punishment
	<i>If the candidate:</i>	
1. (a)	Possesses or keeps accessible in examination hall, any paper, note book, programmable calculators, Cell phones, pager, palm computers or any other form of material concerned with or related to the subject of the examination (theory or practical) in which he is appearing but has not made use of (material shall include any marks on the body of the candidate which can be used as an aid in the subject of the examination)	Expulsion from the examination hall and cancellation of the performance in that subject only.
(b)	Gives assistance or guidance or receives it from any other candidate orally or by any other body language methods or communicates through cell phones with any candidate or persons in or outside the exam hall in respect of any matter.	Expulsion from the examination hall and cancellation of the performance in that subject only of all the candidates involved. In case of an outsider, he will be handed over to the police and a case is registered against him.
2.	Has copied in the examination hall from any paper, book, programmable calculators, palm computers or any other form of material relevant to the subject of the examination (theory or practical) in which the candidate is appearing.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted to appear for the remaining examinations of the subjects of that Semester. The Hall Ticket of the candidate is to be cancelled.
3.	Impersonates any other candidate in connection with the examination.	The candidate who has impersonated shall be expelled from examination hall. The candidate is also debarred and forfeits the seat. The performance of the original candidate, who has been impersonated, shall be cancelled in all the subjects of the examination (including practicals and project work) already appeared and shall not be allowed to appear for examinations

		of the remaining subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat. If the imposter is an outsider, he will be handed over to the police and a case is registered against him.
4.	Smuggles in the Answer book or additional sheet or takes out or arranges to send out the question paper during the examination or answer book or additional sheet, during or after the examination.	Expulsion from the examination hall and cancellation of performance in that subject and all the other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat.
5.	Uses objectionable, abusive or offensive language in the answer paper or in letters to the examiners or writes to the examiner requesting him to award pass marks.	Cancellation of the performance in that subject.
6.	Refuses to obey the orders of the Chief Superintendent/Assistant Superintendent / any officer on duty or misbehaves or creates disturbance of any kind in and around the examination hall or organizes a walk out or instigates others to walk out, or threatens the officer-in charge or any person on duty in or outside the examination hall or any injury to his person or to any of his relations whether by words, either spoken or written or by signs or by visible representation, assaults the officer-incharge, or any person on duty in or outside the examination hall or any of his relations, or indulges in any other act of misconduct or mischief which	In case of students of the college, they shall be expelled from examination halls and cancellation of their performance in that subject and all other subjects the candidate(s) has (have) already appeared and shall not be permitted to appear for the remaining examinations of the subjects of that semester/year. The candidates also are debarred and forfeit their seats. In case of outsiders, they will be handed over to the police and a police case is registered against them.

	result in damage to or destruction of property in the examination hall or any part of the College campus or engages in any other act which in the opinion of the officer on duty amounts to use of unfair means or misconduct or has the tendency to disrupt the orderly conduct of the examination.	
7.	Leaves the exam hall taking away answer script or intentionally tears of the script or any part thereof inside or outside the examination hall.	Expulsion from the examination hall and cancellation of performance in that subject and all the other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat.
8.	Possess any lethal weapon or firearm in the examination hall.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred and forfeits the seat.
9.	If student of the college, who is not a candidate for the particular examination or any person not connected with the college indulges in any malpractice or improper conduct mentioned in clause 6 to 8.	Student of the colleges expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred and forfeits the seat. Person(s) who do not belong to the College will be handed over to police and, a police case will be registered against them.

10.	Comes in a drunken condition to the examination hall.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester.
11.	Copying detected on the basis of internal evidence, such as, during valuation or during special scrutiny.	Cancellation of the performance in that subject and all other subjects the candidate has appeared including practical examinations and project work of that semester/year examinations.
12.	If any malpractice is detected which is not covered in the above clauses 1 to 11 shall be reported to the University for further action to award suitable punishment.	



JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES
M. TECH. (POWER SYSTEM CONTROL AND AUTOMATION)
COURSE STRUCTURE AND SYLLABUS

I Year – I Semester

Category	Course Title	Int. marks	Ext. marks	L	P	C
Core Course I	Advanced Power System Analysis	40	60	4	--	4
Core Course II	Advanced Power System Protection	40	60	4	--	4
Core Course III	Modern Control Theory	40	60	4	--	4
Core Elective I	1. EHV AC Transmission 2. High Voltage Engineering 3. Advanced Digital Signal Processing	40	60	4	--	4
Core Elective II	1. Power Quality 2. Microcontrollers and applications 3. Distribution Automation	40	60	4	--	4
Open Elective I	1. Optimization Techniques 2. Digital control systems 3. Renewable energy systems	40	60	4	--	4
Laboratory I	Power Systems simulation Lab	40	60	--	4	2
Seminar I	Seminar-I	50	--	--	4	2
Total Credits				24	8	28

I Year – II Semester

Category	Course Title	Int. marks	Ext. marks	L	P	C
Core Course IV	Power System Dynamics	40	60	4	--	4
Core Course V	Flexible AC Transmission Systems (FACTS)	40	60	4	--	4
Core Course VI	Power System Operation and Deregulation	40	60	4	--	4
Core Elective III	1. Gas Insulated Systems(GIS) 2. Electric smart grid 3. Energy Auditing, Conservation & Management	40	60	4	--	4
Core Elective IV	1. Reactive Power Compensation and Management 2. Power System Reliability 3. Voltage Stability	40	60	4	--	4
Open Elective II	1. AI Techniques in Electrical Engineering 2. Analysis of power converters 3. Embedded Systems	40	60	4	--	4
Laboratory II	Power Systems Lab	40	60	--	4	2
Seminar II	Seminar-II	50	--	--	4	2
Total Credits				24	8	28

II Year - I Semester

Course Title	Int. marks	Ext. marks	L	P	C	
Comprehensive Viva-Voce	--	100	--	--	4	
Project work Review I	50	--	--	24	12	
Total Credits				--	24	16

II Year - II Semester

Course Title	Int. marks	Ext. marks	L	P	C	
Project work Review II	50	--	--	8	4	
Project Evaluation (Viva-Voce)	--	150	--	16	12	
Total Credits				--	24	16



JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES
M. TECH – I YEAR – I SEM (PSC&A)

ADVANCED POWER SYSTEM ANALYSIS
(Core Course I)

UNIT-I:

Admittance Model and Network Calculations, Branch and Node Admittances, Mutually Coupled Branches in Y_{BUS} , An Equivalent Admittance Network, Modification of Y_{BUS} , Network Incidence Matrix and Y_{BUS} , Method of Successive Elimination, Node Elimination, Triangular Factorization, Sparsity and Near Optimal Ordering.

UNIT-II:

Impedance Model and Network Calculations, the BUS Admittance and Impedance Matrices, Thevenin's Theorem and Z_{BUS} , Algorithms for building Z_{BUS} Modification of existing Z_{BUS} , Calculation of Z_{BUS} elements from Y_{BUS} , Power Invariant Transformations, Mutually Coupled Branches in Z_{BUS} .

UNIT-III:

Gauss Seidel method, N-R Method, Decoupled method, fast decoupled method, comparison between power flow solutions. DC load flow.

UNIT-IV:

Z_{BUS} Method in Contingency Analysis, Adding and Removing Multiple Lines, Piecewise Solution of Interconnected Systems, Analysis of Single Contingencies, Analysis of Multiple Contingencies, Contingency Analysis of DC Model, System Reduction for Contingency and Fault Studies.

UNIT-V:

Fault Analysis: Symmetrical faults-Fault calculations using Z_{BUS} - Fault calculations using Z_{BUS} equivalent circuits –Selection of circuit breakers- Unsymmetrical faults-Problems on various types of faults.

TEXT BOOKS:

2. John J.Grainger and W.D. Stevenson, "Power System Analysis"- T.M.H.Edition.
3. Modern Power System Analysis – by I.J.Nagrath & D.P.Kothari Tata M Graw – Hi Publishing Company Ltd, 2nd edition.

REFERENCE:

1. Power System Analysis and Design by J.Duncan Glover and M.S.Sarma., cengage 3rd Edition.
2. Olle. L.Elgard, "Electrical Energy Systems Theory"-T.M.H.Edition.
3. Power systems stability and control, Prabha Kundur, The Mc Graw – Hill companies.
4. Power System Operation and Control, Dr. K. Uma Rao, Wiley India Pvt. Ltd.
5. Operation and Control in Power Systems, PSR Murthy, Bs Publications.
6. Power System Operation, Robert H. Miller, James H. Malinowski, The Mc Graw – Hill companies.
7. Power Systems Analysis, operation and control by Abhijit Chakrabarti, Sunitha Halder, PHI 3/e , 2010



JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES
M. TECH – I YEAR – I SEM (PSC&A)

ADVANCED POWER SYSTEM PROTECTION
(Core Course II)

UNIT-I:

Static Relays: Advantages of static relays-Basic construction of static relays-Level detectors-Replica impedance –Mixing circuits-General equation for two input phase and amplitude comparators-Duality between amplitude and phase comparators.

Amplitude Comparators: Circulating current type and opposed voltage type- rectifier bridge comparators, Direct and Instantaneous comparators.

UNIT-II:

Phase Comparators: Coincidence circuit type- block spike phase comparator, techniques to measure the period of coincidence-Integrating type-Rectifier and Vector product type- Phase comparators.

Static Over Current Relays: Instantaneous over-current relay-Time over-current relays-basic principles –definite time and Inverse definite time over-current relays.

UNIT-III:

Static Differential Relays: Analysis of Static Differential Relays –Static Relay schemes –Duo bias transformer differential protection –Harmonic restraint relay.

Static Distance Relays: Static impedance-reactance–MHO and angle impedance relay-sampling comparator –realization of reactance and MHO relay using sampling comparator.

UNIT-IV:

Multi-Input Comparators: Conic section characteristics-Three input amplitude comparator –Hybrid comparator-switched distance schemes –Poly phase distance schemes- phase fault scheme –three phase scheme – combined and ground fault scheme.

Power Swings: Effect of power swings on the performance of distance relays –Power swing analysis-Principle of out of step tripping and blocking relays-effect of line and length and source impedance on distance relays.

UNIT-V:

Microprocessor based Protective Relays: (Block diagram and flowchart approach only)-Over current relays–impedance relays-directional relay-reactance relay .Generalized mathematical expressions for distance relays-measurement of resistance and reactance –MHO and offset MHO relays-Realization of MHO characteristics- Realization of offset MHO characteristics -Basic principle of Digital computer relaying.

TEXT BOOKS:

1. Badri Ram and D.N.Vishwakarma, “Power system protection and Switch gear “, TMH publication New Delhi 1995.
2. T.S.Madhava Rao , “Static relays”, TMH publication, second edition 1989.

REFERENCE:

1. Protection and Switchgear, Bhavesh Bhalja, R. P. Mahesheari, Nilesh G. Chothani, Oxford University Press.
2. Electrical Power System Protection, C. Christopoulos and A. Wright, Springer International.



JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES
M. TECH – I YEAR – I SEM (PSC&A)

MODERN CONTROL THEORY
(Core Course III)

UNIT-I:

Mathematical Preliminaries: Fields, Vectors and Vector Spaces – Linear combinations and Bases – Linear Transformations and Matrices – Scalar Product and Norms – Eigen-values, Eigen Vectors and a Canonical form representation of Linear operators – The concept of state – State Equations for Dynamic systems – Time invariance and Linearity – Non-uniqueness of state model – State diagrams for Continuous-Time State models.

UNIT-II:

State Variable Analysis: Linear Continuous time models for Physical systems– Existence and Uniqueness of Solutions to Continuous-Time State Equations – Solutions of Linear Time Invariant Continuous-Time State Equations – State transition matrix and its properties. General concept of controllability – General concept of Observability – Controllability tests for Continuous-Time Invariant Systems – Observability tests for Continuous-Time Invariant Systems – Controllability and Observability of State Model in Jordan Canonical form – Controllability and Observability Canonical forms of State model.

UNIT-III:

Non Linear Systems: Introduction – Non Linear Systems - Types of Non-Linearities – Saturation – Dead-Zone - Backlash – Jump Phenomenon etc;– Singular Points – Introduction to Linearization of nonlinear systems, Properties of Non-Linear systems – Describing function–describing function analysis of nonlinear systems – Stability analysis of Non-Linear systems through describing functions. Introduction to phase-plane analysis, Method of Isoclines for Constructing Trajectories, singular points, phase-plane analysis of nonlinear control systems.

UNIT-IV:

Stability Analysis: Stability in the sense of Lyapunov, Lyapunov's stability and Lyapunov's instability theorems - Stability Analysis of the Linear continuous time invariant systems by Lyapunov second method – Generation of Lyapunov functions – Variable gradient method – Krasovskii's method. State feedback controller design through Pole Assignment – State observers: Full order and Reduced order.

UNIT-V:

Optimal Control: Introduction to optimal control - Formulation of optimal control problems – calculus of variations – fundamental concepts, functional, variation of functional – fundamental theorem of theorem of Calculus of variations – boundary conditions – constrained minimization – formulation using Hamiltonian method – Linear Quadratic regulator.

TEXT BOOKS:

1. modern control system theory by m.gopal – new age international -1984
2. Control System Engineering, Nagrath and Gopal - New Age International – Fourth Edition

REFERENCES:

2. Optimal control by Kirck , Dover Publications
3. Advanced Control Theory A. Nagoor Kani, RBA Publications, 1999
4. Modern Control Engineering by Ogata.K – Prentice Hall - 1997



JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES

M. TECH – I YEAR – I SEM (PSC&A)

EHV AC TRANSMISSION (Core Elective- I)

UNIT- I:

E.H.V.A.C. Transmission line trends and preliminary aspect standard transmission voltages – Estimation at line and ground parameters-Bundle conductor systems-Inductance and Capacitance of E.H.V. lines – positive, negative and zero sequence impedance – Line Parameters for Modes of Propagation.

UNIT- II:

Electrostatic field and voltage gradients – calculations of electrostatic field of AC lines – effect of high electrostatic field on biological organisms and human beings - surface voltage gradients and maximum gradients of actual transmission lines – voltage gradients on sub conductor.

UNIT- III:

Electrostatic induction in unenergized lines – measurement of field and voltage gradients for three phase single and double circuit lines – un energized lines. Power Frequency Voltage control and over-voltages in EHV lines: No load voltage – charging currents at power frequency-voltage control – shunt and series compensation – static VAR compensation.

UNIT - IV:

Corona in E.H.V. lines – Corona loss formulae- attention of traveling waves due to Corona – Audio noise due to Corona, its generation, characteristic and limits. Measurements of audio noise radio interference due to Corona - properties of radio noise – frequency spectrum of RI fields – Measurements of RI and RIV.

UNIT- V:

Design of EHV lines based on steady state and transient limits - EHV cables and their characteristics.

TEXT BOOKS:

1. R. D. Begamudre, "EHVAC Transmission Engineering", New Age International (p) Ltd. 3rd Edition.
2. K.R. Padiyar, "HVDC Power Transmission Systems" New Age International (p) Ltd. 2nd revised Edition, 2012.

REFERENCES:

1. S. Rao "EHVAC and HVDC Transmission Engg. Practice" Khanna publishers.
2. Arrillaga.J " High Voltage Direct Current Transmission" 2nd Edition (London) peter Peregrines, IEE, 1998.
3. Padiyar.K.R, " FACTS Controllers in Power Transmission and Distribution" New Age Int. Publishers, 2007.
4. Hingorani H G and Gyugyi. L " Understanding FACTS-Concepts and Technology of Flexible AC Transmission Systems" New York, IEEE Press, 2000.



JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES
M. TECH – I YEAR – I SEM (PSC&A)

HIGH VOLTAGE ENGINEERING
(Core Elective- I)

UNIT- I:

Introduction To High Voltage Engineering: Electric Field Stresses, Gas / Vacuum as Insulator, Liquid Dielectrics, Solids and Composites, Estimation and Control of Electric Stress, Numerical methods for electric field computation, Surge voltages, their distribution and control, Applications of insulating materials in transformers, rotating machines, circuit breakers, cable power capacitors and bushings.

UNIT- II:

Break Down In Dielectric Materials: Gases as insulating media, collision process, Ionization process, Townsend's criteria of breakdown in gases, Paschen's law. Liquid as Insulator, pure and commercial liquids, breakdown in pure and commercial liquids. Intrinsic breakdown, electromechanical breakdown, thermal breakdown, breakdown of solid dielectrics in practice, Breakdown in composite dielectrics, solid dielectrics used in practice.

UNIT-III:

Generation & Measurement of High Voltages & Currents : Generation of High Direct Current Voltages, Generation of High alternating voltages, Generation of Impulse Voltages, Generation of Impulse currents, Tripping and control of impulse generators. Measurement of High Direct Current voltages, Measurement of High Voltages alternating and impulse, Measurement of High Currents-direct, alternating and Impulse, Oscilloscope for impulse voltage and current measurements.

UNIT-IV:

Over Voltages & Insulation Co-Ordination: Natural causes for over voltages – Lightning phenomenon, Overvoltage due to switching surges, system faults and other abnormal conditions, Principles of Insulation Coordination on High voltage and Extra High Voltage power systems.

UNIT- V:

Testing of Materials & Electrical Apparatus: Measurement of D.C Resistivity, Measurement of Dielectric Constant and loss factor, Partial discharge measurements. Testing of Insulators and bushings, Testing of Isolators and circuit breakers, testing of cables, Testing of Transformers, Testing of Surge Arresters, and Radio Interference measurements.

TEXT BOOKS:

1. High Voltage Engineering by M.S.Naidu and V. Kamaraju – TMH Publications, 3rd Edition
2. High Voltage Engineering: Fundamentals by E.Kuffel, W.S.Zaengl, J.Kuffel by Elsevier, 2nd Edition.

REFERENCES:

1. High Voltage Engineering by C.L.Wadhwa, New Age International (P) Limited, 1997.
2. High Voltage Insulation Engineering by Ravindra Arora, Wolfgang Mosch, New Age International (P) Limited, 1995.
3. High Voltage Engineering, Theory and Practice by Mazen Abdel Salam, Hussein Anis, Ahdan El-Morshedy, Roshdy Radwan, Marcel Dekker



JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES
M. TECH – I YEAR – I SEM (PSC&A)

ADVANCED DIGITAL SIGNAL PROCESSING
(Core Elective- I)

UNIT-I:

Digital Filter Structures: Block diagram representation – Equivalent Structures – FIR and IIR digital filter Structures All pass Filters-tunable IIR Digital Sine-cosine generator- Computational complexity of digital filter structures.

UNIT-II:

Digital Filter Design :

Preliminary considerations- Bilinear transformation method of IIR filter design –design of Low pass high-pass – Band-pass, and Band stop- IIR digital filters – Spectral transformations of IIR filters – FIR filter design –based on Windowed Fourier series – design of FIR digital filters with least – mean square-error – constrained Least –square design of FIR digital filters.

UNIT-III:

DSP Algorithm Implementation : Computation of the discrete Fourier transform- Number representation – Arithmetic operations – handling of overflow – Tunable digital filters – function approximation.

UNIT-IV:

Analysis Of Finite Word Length Effects: The Quantization process and errors-Quantization of fixed –point and floating –point Numbers – Analysis of coefficient Quantization effects – Analysis of Arithmetic Round-off errors- Dynamic range scaling – signal –to- noise in Low –order IIR filters- Low – Sensitivity Digital filter – Reduction of Product round-off errors feedback – Limit cycles in IIR digital filter – Round – off errors in FFT Algorithms.

UNIT-V:

Power Spectrum Estimation : Estimation of spectra from Finite Duration Observations signals-Non-parametric methods for power spectrum Estimation- parametric method for power spectrum Estimation- Estimation of spectral form-Finite duration observation of signals- Non-parametric methods for power spectrum estimation – Walsh methods – Blackman and torchy method.

TEXT BOOKS:

1. Digital Signal Processing principles –algorithms and Applications- john G. Proakis –PHI – 3rd edition 2002.
9. Digital Time Signal Processing: Alan V.Oppenheim,Ronald W ,Shafer – PHI 1996 1st Edition reprint
10. Advanced Digital Signal Processing – Theory and Applications – Glenn Zelniker, Fred J. Taylor.

REFERENCE BOOKS

- 1.Digital Signal Processing – S Salivahanan . A Vallavaraj C. Gnanapriya –TMH – 2nd reprint 2001.
2. Digital Signal Processing – sanjit K.Mitra – TMH second edition.
- 3 Theory and Applications of Digital Signal Processing – Lourens R RebinarandBernold.
4. Digital Filter Analysis and Design Auntoniam - TMH
5. Digital Signal Processing – J.S.Chitode – First Edition, 2008, Technical Publications.



JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES
M. TECH – I YEAR – I SEM (PSC&A)

POWER QUALITY
(Core Elective- II)

UNIT-I:

Introduction : Introduction of the Power Quality (PQ) problem, Terms used in PQ: Voltage, Sag, Swell, Surges, Harmonics, over voltages, spikes, Voltage fluctuations, Transients, Interruption, overview of power quality phenomenon, Remedies to improve power quality, power quality monitoring.

UNIT-II:

Long & Short Interruptions: Interruptions – Definition – Difference between failures, outage, Interruptions – causes of Long Interruptions – Origin of Interruptions – Limits for the Interruption frequency – Limits for the interruption duration – costs of Interruption – Overview of Reliability evaluation to power quality, comparison of observations and reliability evaluation.

Short interruptions: definition, origin of short interruptions, basic principle, fuse saving, voltage magnitude events due to re-closing, voltage during the interruption, monitoring of short interruptions, difference between medium and low voltage systems. Multiple events, single phase tripping – voltage and current during fault period, voltage and current at post fault period, stochastic prediction of short interruptions.

UNIT III:

1 & 3-Phase Voltage SAG Characterization : Voltage sag – definition, causes of voltage sag, voltage sag magnitude, and monitoring, theoretical calculation of voltage sag magnitude, voltage sag calculation in non-radial systems, meshed systems, and voltage sag duration.

Three phase faults, phase angle jumps, magnitude and phase angle jumps for three phase unbalanced sags, load influence on voltage sags.

UNIT-IV:

Power Quality Considerations in Industrial Power Systems: Voltage sag – equipment behavior of Power electronic loads, induction motors, synchronous motors, computers, consumer electronics, adjustable speed AC drives and its operation. Mitigation of AC Drives, adjustable speed DC drives and its operation, mitigation methods of DC drives.

UNIT-V:

Mitigation of Interruptions & Voltage Sags: Overview of mitigation methods – from fault to trip, reducing the number of faults, reducing the fault clearing time changing the power system, installing mitigation equipment, improving equipment immunity, different events and mitigation methods. System equipment interface – voltage source converter, series voltage controller, shunt controller, combined shunt and series controller.

Power Quality and EMC Standards: Introduction to standardization, IEC Electromagnetic compatibility standards, European voltage characteristics standards, PQ surveys.

TEXTBOOKS:

1. Math H J Bollen “Understanding Power Quality Problems”, IEEE Press.
2. R.C. Dugan, M.F. McGranaghan and H.W. Beaty, “Electric Power Systems Quality.” New York: McGraw-Hill.1996

REFERENCES:

1. G.T. Heydt, „Electric Power Quality”, 2nd Edition. (West Lafayette, IN, Stars in a Circle Publications, 1994).
2. Power Quality VAR Compensation in Power Systems, R. SastryVedamMulukutla S. Sarma,CRC Press.
3. A Ghosh, G. Ledwich, Power Quality Enhancement Using Custom Power Devices. Kluwer Academic, 2002



JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES
M. TECH – I YEAR – I SEM (PSC&A)

MICROCONTROLLERS AND APPLICATIONS
(Core Elective- II)

UNIT-I:

Overview of Architecture & Microcontroller Resources: Architecture of a microcontroller – Microcontroller resources – Resources in advanced and next generation microcontrollers – 8051 microcontroller – Internal and External memories – Counters and Timers – Synchronous serial-cum asynchronous serial communication - Interrupts.

UNIT-II:

8051- Microcontrollers Instruction Set : Basic assembly language programming – Data transfer instructions – Data and Bit-manipulation instructions – Arithmetic instructions – Instructions for Logical operations on the test among the Registers, Internal RAM, and SFRs – Program flow control instructions – Interrupt control flow.

UNIT-III:

Real Time Control: Interrupts: Interrupt handling structure of an MCU – Interrupt Latency and Interrupt deadline – Multiple sources of the interrupts – Non-maskable interrupt sources – Enabling or disabling of the sources – Polling to determine the interrupt source and assignment of the priorities among them – Interrupt structure in Intel 8051.

Timers: Programmable Timers in the MCU"s – Free running counter and real time control – Interrupt interval and density constraints.

UNIT-IV:

Systems Design: Digital and Analog Interfacing Methods: Switch, Keypad and Keyboard interfacings – LED and Array of LEDs – Keyboard-cum-Display controller (8279) – Alphanumeric Devices – Display Systems and its interfaces – Printer interfaces – Programmable instruments interface using IEEE 488 Bus – Interfacing with the Flash Memory – Interfaces – Interfacing to High Power Devices – Analog input interfacing – Analog output interfacing – Optical motor shaft encoders – Industrial control – Industrial process control system – Prototype MCU based Measuring instruments – Robotics and Embedded control – Digital Signal Processing and digital filters.

UNIT-V:

Real Time Operating System for Microcontrollers: Real Time operating system – RTOS of Keil (RTX51) – Use of RTOS in Design – Software development tools for Microcontrollers.

16-Bit Microcontrollers: Hardware – Memory map in Intel 80196 family MCU system – IO ports – Programmable Timers and High-speed outputs and input captures – Interrupts – instructions. ARM 32 Bit MCUs: Introduction to 16/32 Bit processors – ARM architecture and organization – ARM / Thumb programming model – ARM / Thumb instruction set – Development tools.

TEXT BOOKS:

1. Raj Kamal," Microcontrollers Architecture, Programming, Interfacing and System Design"– Pearson Education, 2005.
2. Mazidi and Mazidi, "The 8051 Microcontroller and Embedded Systems" – PHI, 2000.

REFERENCES:

1. A.V. Deshmuk, "Microcontrollers (Theory & Applications)" – WTMH, 2005.
2. John B. Peatman, "Design with PIC Microcontrollers" – Pearson Education, 2005.
3. Microcontroller Programming, Julio Sanchez, Maria P. Canton, CRC Press.
4. The 8051 Microcontroller, Ayala, Cengage Learning.
5. Microprocessors and Microcontrollers, Architecture, Programming and System Design, Krishna Kant, PHI Learning PVT. Ltd.
6. Microprocessors, Nilesh B. Bahadure, PHI Learning PVT. Ltd.



JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES
M. TECH – I YEAR – I SEM (PSC&A)

DISTRIBUTION AUTOMATION
(Core Elective- II)

UNIT-I:

Distribution Automation and The Utility System: Introduction to Distribution Automation (DA), control system interfaces, control and data requirements, centralized (Vs) decentralized control, DA System (DAS), DA Hardware, DAS software.

UNIT-II:

Distribution Automation Functions : DA capabilities, Automation system computer facilities, management processes, Information management, system reliability management, system efficiency management, voltage management, Load management.

UNIT-III:

Communication Systems for DA: DA communication requirements, Communication reliability, Cost effectiveness, Data rate Requirements, Two way capability, Ability to communicate during outages and faults, Ease of operation and maintenance, Conforming to the architecture of data flow **Communication systems used in DA :** Distribution line carrier (Power line carrier), Ripple control, Zero crossing technique, telephone, cable TV, Radio, AM broadcast, FM SCA, VHF Radio, UHF Radio, Microwave satellite. Fiber optics, Hybrid Communication systems, Communication systems used in field tests.

UNIT-IV:

Technical Benefits: DA benefit categories, Capital deferred savings, Operation and Maintenance savings, Interruption related savings, Customer related savings, Operational savings, improved operation, Function benefits, Potential benefits for functions, and function shared benefits, Guidelines for formulation of estimating equations Parameters required, economic impact areas, Resources for determining benefits impact on distribution system, integration of benefits into economic evaluation.

UNIT-V:

Economic Evaluation Methods: Development and evaluation of alternate plans, Select study area, Select study period, Project load growth, Develop Alternatives, Calculate operating and maintenance costs, Evaluate alternatives. Economic comparison of alternate plans, Classification of expenses and capital expenditures, Comparison of revenue requirements of alternative plans, Book Life and Continuing plant analysis, Year by year revenue requirement analysis, short term analysis, end of study adjustment, Break even analysis, Sensitivity analysis computational aids.

TEXT BOOKS:

1. Control and Automation of Electrical Distribution Systems, James. Northcote – Green Robert Wilson, CRC Press.
2. Electric Power Distribution Automation, Dr. M. K. Khedkar, Dr. G.M.Dhole, University Science press.

REFERENCES:

1. IEEE Tutorial Course “Distribution Automation”
2. IEEE Working Group on “Distribution Automation”



JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES
M. TECH – I YEAR – I SEM (PSC&A)

OPTIMIZATION TECHNIQUES
(Open Elective I)

UNIT – I

Introduction and Classical Optimization Techniques: Statement of an Optimization problem – design vector – design constraints – constraint surface – objective function – objective function surfaces – classification of Optimization problems.

Classical Optimization Techniques: Single variable Optimization – multi variable Optimization without constraints – necessary and sufficient conditions for minimum/maximum – multivariable Optimization with equality constraints.

Solution by method of Lagrange multipliers – multivariable Optimization with inequality constraints – Kuhn – Tucker conditions.

UNIT – II

Linear Programming: Standard form of a linear programming problem – geometry of linear programming problems – definitions and theorems – solution of a system of linear simultaneous equations – pivotal reduction of a general system of equations – motivation to the simplex method – simplex algorithm.

UNIT – III

Transportation Problem: Finding initial basic feasible solution by north – west corner rule, least cost method and Vogel's approximation method – testing for optimality of balanced transportation problems.

Unconstrained Nonlinear Programming: One – dimensional minimization methods: Classification, Fibonacci method and Quadratic interpolation method

UNIT – IV

Unconstrained Optimization Techniques: Univariate method, Powell's method and steepest descent method.

Constrained Nonlinear Programming: Characteristics of a constrained problem, Classification, Basic approach of Penalty Function method; Basic approach of Penalty Function method; Basic approaches of Interior and Exterior penalty function methods. Introduction to convex Programming Problem.

UNIT – V

Dynamic Programming: Dynamic programming multistage decision processes – types – concept of sub optimization and the principle of optimality – computational procedure in dynamic programming – examples illustrating the calculus method of solution - examples illustrating the tabular method of solution.

TEXT BOOKS:

1. "Engineering optimization: Theory and practice"-by S. S.Rao, New Age International (P) Limited, 3rd edition, 1998.
2. "Introductory Operations Research" by H.S. Kasene & K.D. Kumar, Springer(India), Pvt .Ltd.

REFERENCE BOOKS:

- 1 " Optimization Methods in Operations Research and systems Analysis" – by K.V. Mital and C. Mohan, New Age International (P) Limited, Publishers, 3rd edition, 1996.
2. Operations Research – by Dr. S.D.Sharma.
3. "Operations Research : An Introduction" – by H.A. Taha, PHI Pvt. Ltd., 6th edition
4. Linear Programming – by G. Hadley



JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES
M. TECH – I YEAR – I SEM (PSC&A)

DIGITAL CONTROL SYSTEMS
(Open Elective I)

UNIT – I:

Concept & Representation of Discrete time Systems: Block Diagram of typical control system- advantages of sampling in control systems – examples of discrete data and digital systems – data conversion and quantization – sample and hold devices – D/A and A/D conversion – sampling theorem – reconstruction of sampled signals.

Z-transform: Definition of Z-transforms – mapping between s-plane and z-plane –inverse z- transform – properties of z-transforms - ROC of z-transforms –pulse transfer function –relation between $G(s)$ and $G(z)$ – signal flow graph method applied to digital control systems.

UNIT- II:

State Space Analysis: State space modeling of discrete time systems – state transition equation of discrete time invariant systems – solution of time invariant discrete state equations: recursive method and the Z-Transformation method – conversion of pulse transfer function to the state model & vice-versa – Eigen values – Eigen vectors of discrete time system-matrix (A) – Realization of pulse transformation in state space form, discretization of continuous time systems, Computation of state transition matrix and its properties. Response of sample data system between sampling instants.

UNIT – III :

Controllability, Observability & Stability Tests: Concept of controllability, stabilizability, observability and reachability - Controllability and observability tests, Transformation of discrete time systems into controllable and observable forms.

Stability: Definition of stability – stability tests – The second method of Liapunov.

UNIT- IV:

Design of Discrete Time Controllers and Observers: Design of discrete time controller with bilinear transformation – Realization of digital PID controller-Design of deadbeat controller; Pole placement through state feedback.

UNIT-V:

State Observers: Design of - Full order and reduced order observers. Study of observer based control design

TEXT BOOKS:

1. K. Ogata , Discrete-Time Control systems, Pearson Education/PHI, 2nd Edition.
2. V. I. George, C. P. Kurian, Digital Control Systems, Cengage Learning.
3. M.Gopal, Digital Control Engineering, New Age Int. Pvt. Ltd., 2014

REFERENCE BOOKS:

1. Kuo, Digital Control Systems, Oxford University Press, 2nd Edition, 2003.
2. M.Gopal , Digital Control and State Variable Methods, TMH.
3. M. Sami Fadali Antonio Visioli, Digital Control Engineering Analysis and Design, Academic Press



JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES

M. TECH – I YEAR – I SEM (PSC&A)

RENEWABLE ENERGY SYSTEMS (Open Elective I)

UNIT-I:

Photo voltaic power generation ,spectral distribution of energy in solar radiation, solar cell configurations, voltage developed by solar cell, photo current and load current, practical solar cell performance, commercial photo voltaic systems, test specifications for PV systems, applications of super conducting materials in electrical equipment systems.

UNIT-II:

Principles of MHD power generation, ideal MHD generator performance, practical MHD generator, MHD technology.

Wind Energy conversion: Power from wind, properties of air and wind, types of wind Turbines, operating characteristics.

UNIT-III:

Tides and tidal power stations, modes of operation, tidal project examples, turbines and generators for tidal power generation.

Wave energy conversion: properties of waves and power content, vertex motion of Waves, device applications. Types of ocean thermal energy conversion systems Application of OTEC systems examples,

UNIT-IV:

Miscellaneous energy conversion systems: coal gasification and liquefaction, biomass conversion, geothermal energy, thermo electric energy conversion, principles of EMF generation, description of fuel cells, Co-generation and energy storage, combined cycle co-generation, energy storage.

Global energy position and environmental effects: energy units, global energy position.

UNIT-V:

Types of fuel cells, H₂-O₂ Fuel cells, Application of fuel cells – Batteries, Description of batteries, Battery application for large power. Environmental effects of energy conversion systems, pollution from coal and preventive measures steam stations and pollution, pollution free energy systems.

TEXT BOOKS:

1. “Energy conversion systems” by Rakosh das Begamudre, New age International publishers, New Delhi - 2000.
2. “Renewable Energy Resources” by John Twidell and Tony Weir, 2nd Edition, Fspn & Co.

REFERENCES:

1. “Understanding Renewable Energy Systems” by Volker Quaschnig, 2005, UK.
2. “Renewable Energy Systems-Advanced Conversion, Technologies & Applications” by Faner Lin Luo Honer Ye, CRC press, Taylor & Francies group.



JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES
M. TECH – I YEAR – I SEM (PSC&A)

POWER SYSTEMS SIMULATION LAB

All the Ten Experiments are Compulsory:

1. Develop Program for Y_{BUS} formation.
2. Develop Program for G-S Load Flow Analysis.
3. Develop Program for N-R Load Flow Analysis.
4. Develop Program for FDLF Load Flow Analysis.
5. Develop Program for Short Circuit Analysis.
6. Develop Program for Transient Stability Analysis for Single Machine connected to Infinite Bus by Point by Point Method.
7. Develop Simulation of RLC Circuit
8. Develop Simulation of Single Phase Full Converter with RLE Load
9. Develop Program model for Closed Loop Speed Control of Separately Excited D.C Motor.
10. Develop Program model for Sinusoidal Pulse Width Modulation.



JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES

M. TECH – I YEAR – II SEM (PSC&A)

POWER SYSTEM DYNAMICS (Core Course IV)

UNIT-I: BASIC CONCEPTS

Power system stability states of operation and system security - system dynamics - problems system model analysis of steady State stability and transient stability - simplified representation of Excitation control.

UNIT-II: MODELING OF SYNCHRONOUS MACHINE

Synchronous machine - park's Transformation-analysis of steady state performance per - unit quantities-Equivalent circuits of synchronous machine-determination of parameters of equivalent circuits.

UNIT-III: EXCITATION SYSTEM

Excitation system modeling-excitation systems block Diagram - system representation by state equations- Dynamics of a synchronous generator connected to infinite bus - system model Synchronous machine model-stator equations rotor equations - Synchronous machine model with field circuit - one equivalent damper winding on q axis (model 1.1) - calculation of Initial conditions.

UNIT-IV: ANALYSIS OF SINGLE MACHINE SYSTEM

Small signal analysis with block diagram - Representation Characteristic equation and application of Routh Hurwitz criterion- synchronizing and damping torque analysis-small signal model - State equations.

UNIT-V: APPLICATION OF POWER SYSTEM STABILIZERS

Basic concepts in applying PSS - Control signals - Structure and tuning of PSS - Washout circuit - Dynamic compensator analysis of single machine infinite bus system with and without PSS.

TEXT BOOK:

1. K.R. PADIYAR," Power system dynamics "- B.S. Publications.

REFERENCE BOOKS:

1. P.M. Anderson and A.A. Fouad,"Power system control and stability ",IEEE Press
2. R. Ramanujam, "Power Systems Dynamics"- PHI Publications.



JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES

M. TECH – I YEAR – II SEM (PSC&A)

FLEXIBLE AC TRANSMISSION SYSTEMS (FACTS)

(Core Course V)

UNIT-I: FACTS CONCEPTS

Transmission interconnections power flow in an AC system, loading capability limits, Dynamic stability considerations, importance of controllable parameters basic types of FACTS controllers, benefits from FACTS controllers.

UNIT-II: VOLTAGE SOURCE CONVERTERS

Single phase three phase full wave bridge converters transformer connections for 12 pulse 24 and 48 pulse operation. Three level voltage source converter, pulse width modulation converter, basic concept of current source Converters, and comparison of current source converters with voltage source converters.

UNIT-III: STATIC SHUNT COMPENSATION

Objectives of shunt compensation, mid-point voltage regulation voltage instability prevention, improvement of transient stability, Power oscillation damping, Methods of controllable VAR generation, variable impedance type static VAR generators switching converter type VAR generators hybrid VAR generators.

UNIT-IV: SVC AND STATCOM

The regulation and slope transfer function and dynamic performance, transient stability enhancement and power oscillation damping operating point control and summary of compensator control.

UNIT-V: STATIC SERIES COMPENSATORS

Concept of series capacitive compensation, improvement of transient stability, power oscillation damping, and functional requirements of GTO thyristor controlled series capacitor (GSC), thyristor switched series capacitor (TSSC), and thyristor controlled series capacitor (TCSC) Control schemes for GSC TSSC and TCSC.

TEXT BOOKS:

1. "Understanding FACTS Devices" N.G. Hingorani and L. Gyugi.
IEEE Press Publications 2000.



JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES

M. TECH – I YEAR – II SEM (PSC&A)

POWER SYSTEM OPERATION AND DEREGULATION (Core Course VI)

UNIT- I: OPTIMAL POWER FLOW

Introduction- Solution to the optimal power flow-gradient method-Newton's method-Linear sensitivity analysis- Linear programming methods- Security constrained OPF-Interior point algorithm- Bus incremental costs

UNIT-II: POWER SYSTEM SECURITY

Introduction –Factors affecting power system security-Contingency analysis-Detection of network problems-Linear sensitivity analysis-AC power flow methods-contingency selection-concentric relaxation-Bounding area method

UNIT-III: STATE ESTIMATION IN POWER SYSTEMS

Introduction- Power system state estimation- Maximum likelihood Weighted Least squares estimation-Matrix formulation- State estimation of AC network- State estimation by orthogonal decomposition- detection and identification of Bad measurements- Estimation of quantities not being measured- Network observability and pseudo measurements

UNIT-IV: POWER SYSTEM DEREGULATION

Introduction- motivation for restructuring of power systems- Electricity market entities model-benefits of deregulation- terminology-deregulation in Indian power sector-Operations in power markets-power pools-transmission networks and electricity markets.

UNIT-V: AVAILABLE TRANSFER CAPABILITY

Introduction methods: of determination of ATC - ATC calculation considering the effect of contingency analysis- Transmission open access and pricing-cost components of transmission system- transmission pricing methods-Incremental cost based transmission pricing.

TEXT BOOKS:

1. A.J.Wood & B.F.Woolenberg- John Wiley Power Generation, "Operation and Control"- 2nd edition.
2. P.Venkatesh. B.V.Manikandan, S.Charles Raja- A.Srinivasan, "Electrical power systems: Analysis, security, Deregulation"– PHI 2012.



JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES

M. TECH – I YEAR – II SEM (PSC&A)

GAS INSULATED SYSTEMS (GIS) (Core Elective–III)

UNIT–I: INTRODUCTION TO GIS AND PROPERTIES OF SF₆

Characteristics of GIS- Introduction to SF₆ - Physical properties-Chemical properties - Electrical properties-Specification of SF₆ gas for GIS application - Handling of SF₆ gas before use - Safe handling of SF₆ gas in electrical equipment - Equipment for handling the SF₆ Gas - SF₆ and environment.

UNIT–II: LAYOUT OF GIS STATIONS

Advancement of GIS station - Comparison with Air Insulated Substation - Economics of GIS - User Requirements for GIS - Main Features for GIS - Planning and Installation components of a GIS station.

UNIT–III: DESIGN AND CONSTRUCTION OF GIS STATION

Introduction - Rating of GIS components - Design Features - Estimation of different types of Electrical Stresses -Design Aspects of GIS components - Insulation Design for Components - Insulation Design for GIS - Thermal Considerations in the Design of GIS - Effect of very Fast Transient Over-voltages (VFTO) on the GIS design - Insulation Coordination systems - Gas handling and Monitoring System Design.

UNIT-IV: FAST TRANSIENT PHENOMENA IN GIS

Introduction- Disconnect Switching in Relation to Very fast Transients-Origin of VFTO- Propagation and Mechanism of VFTO-VFTO Characteristics- Effects of VFTO-Testing of GIS for VFTO.

UNIT–V: SPECIAL PROBLEMS IN GIS AND GIS DIAGNOSTICS

Introduction - particles their effects and their control- Insulating Spacers and their Reliability - SF₆ Gas Decomposition - Characteristics of imperfections in insulation - Insulation Diagnostic methods - PD Measurement and UHF Method.

TEXT BOOK:

1. M. S. Naidu, " Gas Insulated Substations"- IK International Publishing House.



JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES

M. TECH – I YEAR – II SEM (PSC&A)

ELECTRIC SMART GRID

(Core Elective – III)

UNIT-I: INTRODUCTION

Introduction to smart grid- Electricity network-Local energy networks- Electric transportation- Low carbon central generation-Attributes of the smart grid- Alternate views of a smart grid.

SMART GRID TO EVOLVE A PERFECT POWER SYSTEM: Introduction- Overview of the perfect power system configurations- Device level power system- Building integrated power systems- Distributed power systems- Fully integrated power system-Nodes of innovation.

UNIT-II: DC DISTRIBUTION AND SMART GRID

AC vs DC sources-Benefits of and drives of DC power delivery systems-Powering equipment and appliances with DC-Data centers and information technology loads-Future neighborhood-Potential future work and research.

INTELLIGRID ARCHITECTURE FOR THE SMARTGRID: Introduction- Launching intelligrid-Intelligrid today- Smart grid vision based on the intelligrid architecture-Barriers and enabling technologies.

UNIT-III: DYNAMIC ENERGY SYSTEMS CONCEPT

Smart energy efficient end use devices-Smart distributed energy resources-Advanced whole building control systems- Integrated communications architecture-Energy management-Role of technology in demand response- Current limitations to dynamic energy management-Distributed energy resources-Overview of a dynamic energy management-Key characteristics of smart devices- Key characteristics of advanced whole building control systems-Key characteristics of dynamic energy management system.

UNIT-IV: ENERGY PORT AS PART OF THE SMART GRID:

Concept of energy -Port, generic features of the energy port.

POLICIES AND PROGRAMS TO ENCOURAGE END – USE ENERGY EFFICIENCY: Policies and programs in action -multinational - national-state-city and corporate levels.

MARKET IMPLEMENTATION: Framework-factors influencing customer acceptance and response - program planning-monitoring and evaluation.

UNIT-V: EFFICIENT ELECTRIC END – USE TECHNOLOGY

ALTERNATIVES

Existing technologies – lighting - Space conditioning - Indoor air quality - Domestic water heating - hyper efficient appliances - Ductless residential heat pumps and air conditioners - Variable refrigerant flow air conditioning-Heat pump water heating - Hyper efficient residential appliances - Data center energy efficiency- LED street and area lighting - Industrial motors and drives - Equipment retrofit and replacement - Process heating - Cogeneration, Thermal energy storage - Industrial energy management programs - Manufacturing process-Electro-technologies, Residential, Commercial and industrial sectors.

TEXT BOOKS:

1. Clark W Gellings, “The Smart Grid, Enabling Energy Efficiency and Demand Side Response”- CRC Press, 2009.
2. Janaka Ekanayake, Kithsiri Liyanage,Jianzhong.Wu, Akihiko Yokoyama, Nick Jenkins, “Smart Grid: Technology and Applications”- Wiley, 2012.
3. James Momoh, “Smart Grid :Fundamentals of Design and Analysis”- Wiley, IEEE Press, 2012.



JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES
M. TECH – I YEAR – II SEM (PSC&A)

ENERGY AUDITING, CONSERVATION AND MANAGEMENT
(Core Elective – III)

UNIT-I: BASIC PRINCIPLES OF ENERGY AUDIT

Energy audit- definitions, concept , types of audit, energy index, cost index ,pie charts, Sankey diagrams, load profiles, Energy conservation schemes- Energy audit of industries- energy saving potential, energy audit of process industry, thermal power station, building energy audit.

UNIT-II: ENERGY MANAGEMENT

Principles of energy management, organizing energy management program, initiating, planning, controlling, promoting, monitoring, reporting- Energy manger, Qualities and functions, language, Questionnaire – check list for top management.

UNIT-III: ENERGY EFFICIENT MOTORS

Energy efficient motors , factors affecting efficiency, loss distribution , constructional details , characteristics - variable speed , variable duty cycle systems, RMS hp- voltage variation-voltage unbalance- over motoring- motor energy audit

**UNIT-IV: POWER FACTOR IMPROVEMENT, LIGHTING
AND ENERGY INSTRUMENTS**

Power factor – methods of improvement, location of capacitors, Pf with non linear loads, effect of harmonics on power factor, power factor motor controllers - Good lighting system design and practice, lighting control ,lighting energy audit - Energy Instruments- wattmeter, data loggers, thermocouples, pyrometers,lux meters, tongue testers ,application of PLC's.

UNIT-V: ECONOMIC ASPECTS AND ANALYSIS

Economics Analysis-Depreciation Methods, time value of money, rate of return , present worth method , replacement analysis, life cycle costing analysis- Energy efficient motors- calculation of simple payback method, net present worth method- Power factor correction, lighting - Applications of life cycle costing analysis, return on investment .

REFERENCE BOOKS:

1. Energy management by W.R. Murphy AND G. Mckay Butter worth, Heinemann publications.
2. Energy management by Paul o' Callaghan, Mc-graw Hill Book company-1st edition, 1998
3. Energy efficient electric motors by John .C. Andreas, Marcel Dekker Inc Ltd-2nd edition, 1995-
4. Energy management hand book by W.C.Turner, John wiley and sons
5. Energy management and good lighting practice : fuel efficiency- booklet12-EEO



JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES

M. TECH – I YEAR – II SEM (PSC&A)

REACTIVE POWER COMPENSATION AND MANAGEMENT

(Core Elective-IV)

UNIT-I: LOAD COMPENSATION

Objectives and specifications – reactive power characteristics – inductive and capacitive approximate biasing – Load compensator as a voltage regulator – phase balancing and power factor correction of unsymmetrical loads- examples.

UNIT-II: STEADY – STATE REACTIVE POWER COMPENSATION IN TRANSMISSION SYSTEM

Uncompensated line – types of compensation – Passive shunt and series and dynamic shunt compensation –examples

Transient state reactive power compensation in transmission systems:

Characteristic time periods – passive shunt compensation – static compensations- series capacitor compensation –compensation using synchronous condensers – examples

UNIT-III: REACTIVE POWER COORDINATION

Objective – Mathematical modeling – Operation planning – transmission benefits – Basic concepts of quality of power supply – disturbances- steady –state variations – effects of under voltages – frequency –Harmonics, radio frequency and electromagnetic interferences

UNIT-IV: DEMAND SIDE MANAGEMENT

Load patterns – basic methods load shaping – power tariffs- KVAR based tariffs penalties for voltage flickers and Harmonic voltage levels

Distribution side Reactive power Management:

System losses –loss reduction methods – examples – Reactive power planning – objectives – Economics Planning capacitor placement – retrofitting of capacitor banks

UNIT-V: USER SIDE REACTIVE POWER MANAGEMENT

KVAR requirements for domestic appliances – Purpose of using capacitors – selection of capacitors – deciding factors – types of available capacitor, characteristics and Limitations

Reactive power management in electric traction systems and arc furnaces:

Typical layout of traction systems – reactive power control requirements – distribution transformers- Electric arc furnaces – basic operations- furnaces transformer –filter requirements – remedial measures –power factor of an arc furnace

REFERENCE BOOKS:

1. Reactive power control in Electric power systems by T.J.E.Miller, John Wiley and sons, 1982 (Units I to IV)
2. Reactive power Management by D.M.Tagare, Tata McGraw Hill, 2004. (Units V to VIII)



JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES

M. TECH – I YEAR – II SEM (PSC&A)

POWER SYSTEM RELIABILITY

(Core Elective-IV)

UNIT-I: GENERATING SYSTEM RELIABILITY ANALYSIS – I

Generation system model – capacity outage probability tables – Recursive relation for capacitive model building – sequential addition method – unit removal – Evaluation of loss of load and energy indices – Examples.

UNIT-II: GENERATING SYSTEM RELIABILITY ANALYSIS – II

Frequency and Duration methods – Evaluation of equivalent transitional rates of identical and non-identical units – Evaluation of cumulative probability and cumulative frequency of non-identical generating units – 2- level daily load representation - merging generation and load models – Examples.

UNIT-III: OPERATING RESERVE EVALUATION

Basic concepts - risk indices – PJM methods – security function approach – rapid start and hot reserve units – Modelling using STPM approach.

Bulk Power System Reliability Evaluation:

Basic configuration – conditional probability approach – system and load point reliability indices – weather effects on transmission lines – Weighted average rate and Markov model – Common mode failures.

UNIT-IV: INTER CONNECTED SYSTEM

RELIABILITY ANALYSIS

Probability array method – Two inter connected systems with independent loads – effects of limited and unlimited tie capacity - imperfect tie – Two connected Systems with correlated loads – Expression for cumulative probability and cumulative frequency.

Distribution System Reliability Analysis – I (Radial configuration):

Basic Techniques – Radial networks –Evaluation of Basic reliability indices, performance indices – load point and system reliability indices – customer oriented, loss and energy oriented indices – Examples.

UNIT-V: DISTRIBUTION SYSTEM RELIABILITY ANALYSIS - II (PARALLEL CONFIGURATION)

Basic techniques – inclusion of bus bar failures, scheduled maintenance – temporary and transient failures – weather effects – common mode failures –Evaluation of various indices – Examples

Substations and Switching Stations:

Effects of short-circuits - breaker operation – Open and Short-circuit failures – Active and Passive failures – switching after faults – circuit breaker model – preventive maintenance – exponential maintenance times.

REFERENCE BOOKS:

1. Reliability Evaluation of Power Systems by Roy Billinton and Ronald N. Allan, Plenum press, New York and London (Second Edition), 1996.
2. Reliability Modeling in Electric Power Systems by J. Endrenyi, John Wiley and Sons, 1978. (First Edition)



JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES

M. TECH – I YEAR – II SEM (PSC&A)

VOLTAGE STABILITY

(Core Elective – IV)

UNIT-I: INTRODUCTION TO VOLTAGE STABILITY

Definitions: Voltage Stability, Voltage Collapse, Voltage Security; Physical relation indicating dependency of voltage on reactive power flow; Factors affecting Voltage collapse and instability; Previous cases of voltage collapse incidences.

UNIT-II: GRAPHICAL ANALYSIS OF VOLTAGE STABILITY

Comparison of Voltage and angular stability of the system; Graphical Methods describing voltage collapse phenomenon: P-V and Q-V curves; detailed description of voltage collapse phenomenon with the help of Q-V curves.

UNIT-III: ANALYSIS OF VOLTAGE STABILITY

Analysis of voltage stability on SMLB system: Analytical treatment and analysis.

Voltage Stability Indices:

Voltage collapse proximity indicator; Determinant of Jacobin as proximity indicators; Voltage stability margin.

UNIT-IV: POWER SYSTEM LOADS

Loads that influences voltage stability: Discharge lights, Induction Motor, Air-conditioning, heat pumps, electronic power supplies, OH lines and cables.

Reactive Power Compensation:

Generation and Absorption of reactive power; Series and Shunt compensation; Synchronous condensers, SVC s; OLTC s; Booster Transformers.

UNIT-V: VOLTAGE STABILITY MARGIN

Stability Margin: Compensated and un-compensated systems.

Voltage Security

Definition; Voltage security; Methods to improve voltage stability and its practical aspects.

TEXT BOOKS:

1. "Performance, operation and control of EHV power transmission system"-A.CHAKRABARTHY, D.P.KOTARI and A.K.MUKOPADYAY, A.H.Wheeler Publishing, I Edition, 1995.
2. "Power System Dynamics: Stability and Control" – K.R.PADIYAR, II Edition, B.S.Publications.

REFERENCE:

1. "Power System Voltage Stability"- C.W.TAYLOR, Mc Graw Hill, 1994.



JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES

M. TECH – I YEAR – II SEM (PSC&A)

AI TECHNIQUES IN ELECTRICAL ENGINEERING (Open Elective – II)

UNIT – I: ARTIFICIAL NEURAL NETWORKS

Introduction-Models of Neural Network - Architectures – Knowledge representation – Artificial Intelligence and Neural networks–Learning process – Error correction learning – Hebbian learning – Competitive learning –Boltzman learning –Supervised learning – Unsupervised learning – Reinforcement learning- learning tasks.

UNIT- II: ANN PARADIGMS

Multi – layer perceptron using Back propagation Algorithm-Self – organizing Map – Radial Basis Function Network – Functional link, network – Hopfield Network.

UNIT – III: FUZZY LOGIC

Introduction – Fuzzy versus crisp – Fuzzy sets - Membership function – Basic Fuzzy set operations – Properties of Fuzzy sets – Fuzzy cartesian Product –Operations on Fuzzy relations – Fuzzy logic – Fuzzy Quantifiers-Fuzzy Inference-Fuzzy Rule based system-Defuzzification methods.

UNIT – IV: GENETIC ALGORITHMS

Introduction-Encoding –Fitness Function-Reproduction operators-Genetic Modeling –Genetic operators-Crossover-Single – site crossover-Two point crossover –Multi point crossover-Uniform crossover – Matrix crossover-Crossover Rate-Inversion & Deletion –Mutation operator –Mutation – Mutation Rate-Bit-wise operators-Generational cycle-convergence of Genetic Algorithm.

UNIT-V: APPLICATIONS OF AI TECHNIQUES

Load forecasting – Load flow studies – Economic load dispatch – Load frequency control – Single area system and two area system – Small Signal Stability (Dynamic stability) Reactive power control – speed control of DC and AC Motors.

TEXT BOOK:

1. S.Rajasekaran and G.A.V.Pai, "Neural Networks, Fuzzy Logic & Genetic Algorithms"-PHI, New Delhi, 2003.

REFERENCE BOOKS:

1. P.D.Wasserman, Van Nostrand Reinhold, "Neural Computing Theory & Practice"- New York, 1989.
2. Bart Kosko, "Neural Network & Fuzzy System" Prentice Hall, 1992.
3. G.J.Klir and T.A.Folger, "Fuzzy sets, Uncertainty and Information"-PHI, Pvt.Ltd, 1994.
4. D.E.Goldberg, " Genetic Algorithms"- Addison Wesley 1999.



JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES

M. TECH – I YEAR – II SEM (PSC&A) ANALYSIS OF POWER ELECTRONIC CONVERTERS (Open Elective II)

Unit I

Single Phase AC Voltage Controllers: Single phase AC voltage controllers with Resistive, Resistive-inductive and Resistive-inductive-induced e.m.f. loads - ac voltage controllers with PW Control - Effects of source and load inductances - Synchronous tap changers-Applications - numerical problems.

Unit II

Three Phase AC Voltage Controllers: Three phase AC voltage controllers - Analysis of controllers with star and delta Connected Resistive, Resistive-inductive loads - Effects of source and load Inductances - applications - numerical problems.

Cycloconverters: Single phase to single phase cycloconverters - analysis of midpoint and bridge Configurations - Three phase to three phase cycloconverters - analysis of Midpoint and bridge configurations - Limitations - Advantages - Applications- numerical problems.

Unit III

Single Phase Converters: Single phase converters - Half controlled and Fully controlled converters - Evaluation of input power factor and harmonic factor - continuous and Discontinuous load current - single phase dual converters - power factor Improvements - Extinction angle control - symmetrical angle control - PWM -single phase sinusoidal PWM - single phase series converters - Applications - Numerical problems.

Three Phase Converters: Three phase converters - Half controlled and fully controlled converters - Evaluation of input power factor and harmonic factor - continuous and Discontinuous load current - three phase dual converters - power factor Improvements - three phase PWM - twelve pulse converters - applications -Numerical problems.

Unit VI

D.C. to D.C. Converters: Analysis of step-down and step-up dc to dc converters with resistive and Resistive-inductive loads - Switched mode regulators - Analysis of Buck Regulators - Boost regulators - buck and boost regulators - Cuk regulators - Condition for continuous inductor current and capacitor voltage - comparison of regulators -Multioutput boost converters - advantages - applications - Numerical problems.

Unit V

Pulse Width Modulated Inverters(single phase): Principle of operation - performance parameters - single phase bridge inverter -evaluation of output voltage and current with resistive, inductive and Capacitive loads - Voltage control of single phase inverters - single PWM - Multiple PWM - sinusoidal PWM - modified PWM - phase displacement Control - Advanced modulation techniques for improved performance - Trapezoidal, staircase, stepped, harmonic injection and delta modulation - Advantage - application - numerical problems.

Pulse Width Modulated Inverters(three phase): Three phase inverters - analysis of 180 degree condition for output voltage And current with resistive, inductive loads - analysis of 120 degree Conduction - voltage control of three phase inverters - sinusoidal PWM - Third Harmonic PWM – 60 degree PWM - space vector modulation - Comparison of PWM techniques - harmonic reductions - Current Source Inverter - variable d.c. link inverter - boost inverter - buck and boost inverter - inverter circuit design - advantages -applications - numerical problems.

TEXT BOOKS:

1. Power Electronics - Mohammed H. Rashid - Pearson Education -Third Edition - First Indian reprint 2004.
2. Power Electronics - Ned Mohan, Tore M. Undeland and William P. Robbins –John Wiley and Sons - Second Edition

REFERENCES:

1. Power Electronics Daniel W. Hart
2. Fundamentals of Power Electronis, 2nd Edition. R.W. Erickson
3. The power electronics Hand Book Timothy, L. Skvarenina, Purdue University



JAYAMUKHI INSTITUTE OF TECHNOLOGICAL SCIENCES
M. TECH – I YEAR – II SEM (PSC&A)

EMBEDDED SYSTEMS
(Open Elective II)

UNIT- I:

Overview of Embedded System: Embedded System, types of Embedded System, Requirements of Embedded System, and Issues in Embedded software development, Applications.

UNIT-II:

Processor & Memory Organization: Structural units in a processor, Processor selection, Memory devices, Memory selection, Memory Allocation & Map, Interfacing.

UNIT-III:

Devices, Device Drivers & Buses for Device Networks: I/O devices, Timer & Counter devices, Serial Communication, Communication between devices using different buses. Device drives, Parallel and serial port device drives in a system, Interrupt servicing mechanism, context and periods for context switching, Deadline and Interrupt Latency.

UNIT-IV:

Programming & Modeling Concepts : Program elements, Modeling Processes for Software Analysis, Programming Models, Modeling of Multiprocessor Systems, Software algorithm Concepts, design, implementation, testing, validating, debugging, Management and maintenance, Necessity of RTOS.

UNIT-V:

Hardware and Software Co-Design: Embedded system design and co design issues in software development, design cycle in development phase for Embedded System, Use of ICE & Software tools for development of ES, Issues in embedded system design.

TEXTBOOK

1. Embedded systems: Architecture, programming and design by Rajkamal, TMH
2. Embedded system design by Arnold S Burger, CMP

REFERENCES

1. An embedded software primer by David Simon, PEA
2. Embedded systems design:Real world design be Steve Heath; Butterworth Heinenann, Newton mass USA 2002
3. Data communication by Hayt.



POWER SYSTEMS LAB

All the Ten Experiments are Compulsory:

1. Determination of X_d and X_q for a 3-phase salient pole Synchronous motor
2. Characteristics of Over Current Relays
 - (i) IDMT Electromagnetic Relay (7051 A)
 - (ii) Microprocessor based Relay (7051 B)
3. Determination of Breakdown strength of oil by Variable Distance Electrodes
4. Determination of Transmission Line Parameters
5. Fault Analysis (LL, LG, LLL) of transmission Lines
6. Determination of Earth resistance under various conditions
7. Characteristics of Over Voltage Relay
 - (i) Electromagnetic Relay (7053 A)
 - (ii) Microprocessor based Relay (7053 B)
8. Characteristics of Under Voltage (UV) and Negative sequence Relays
 - (i) UV Electromagnetic Relay (7052 A)
 - (ii) UV Microprocessor based Relay (7052 B)
 - (iii) Static Negative Sequence Relay (7055 B)
9. Performance and Testing of Generator Protection System.
10. Performance and Testing of Transformer Protection System.